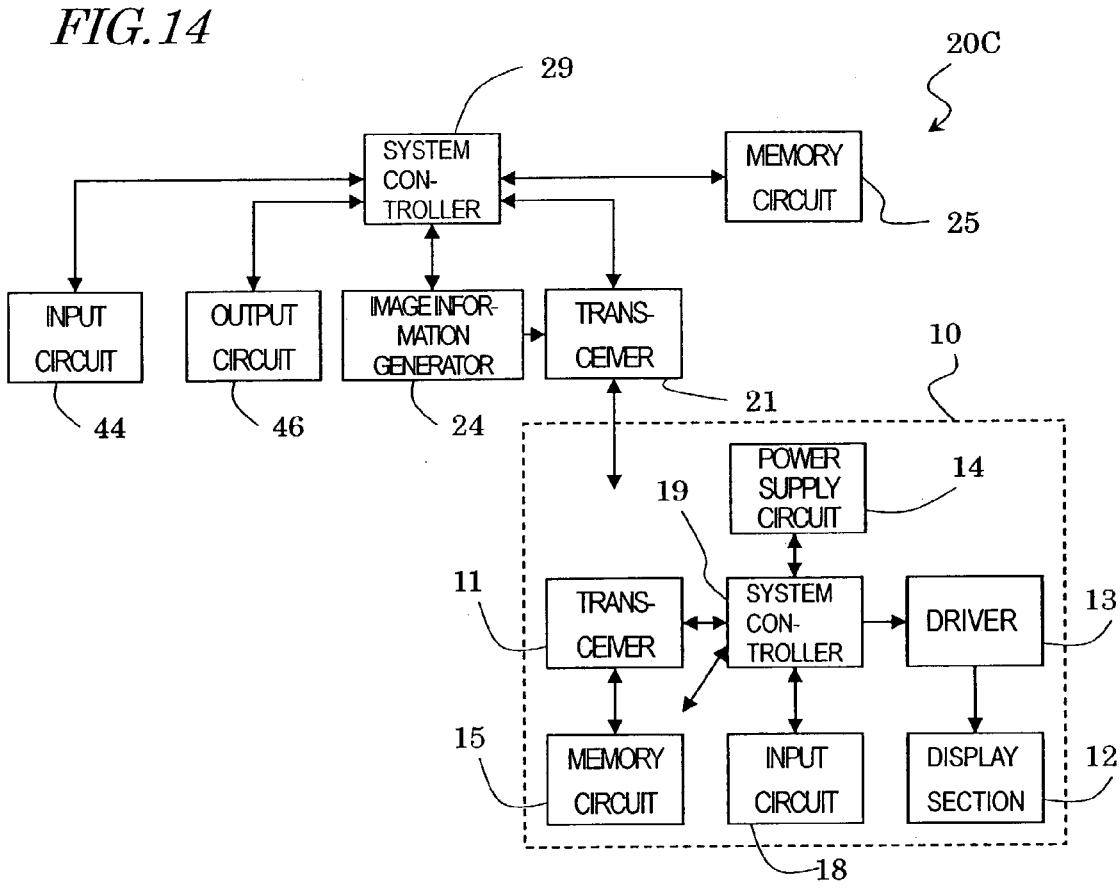
FIG. 14


20C
29
SYSTEM CON-TROLLER
MEMORY CIRCUIT
25
INPUT CIRCUIT
OUTPUT CIRCUIT
IMAGE INFOR-MATION GENERATOR
TRANS-CEIVER
10
21
44
46
24
POWER SUPPLY CIRCUIT
14
19
11
TRANS-CEIVER
SYSTEM CON-TROLLER
DRIVER
13
15
MEMORY CIRCUIT
INPUT CIRCUIT
DISPLAY SECTION
12
18