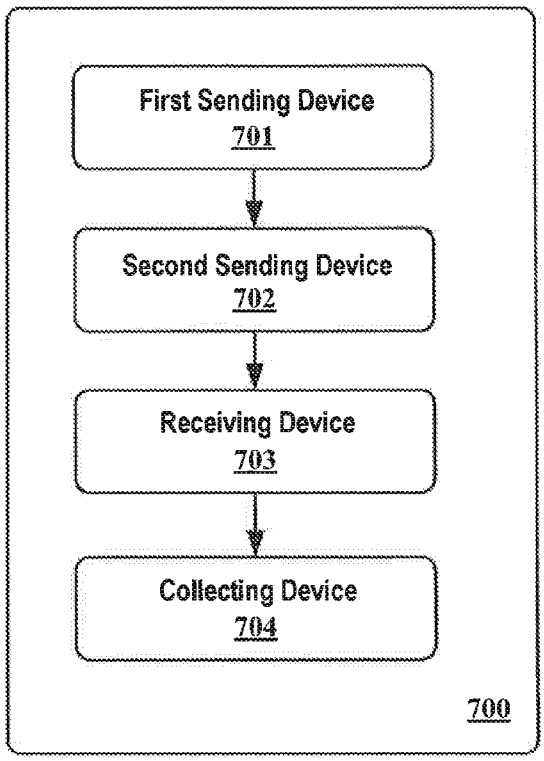
First Sending Device
701
Second Sending Device
702
Receiving Device
703
Collecting Device
704
700

Fig. 7

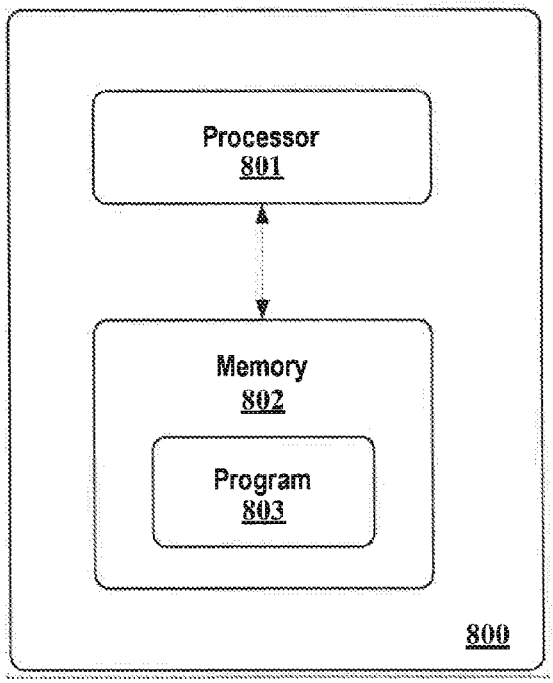
Processor
801
Memory
802
Program
803
800

Fig. 8