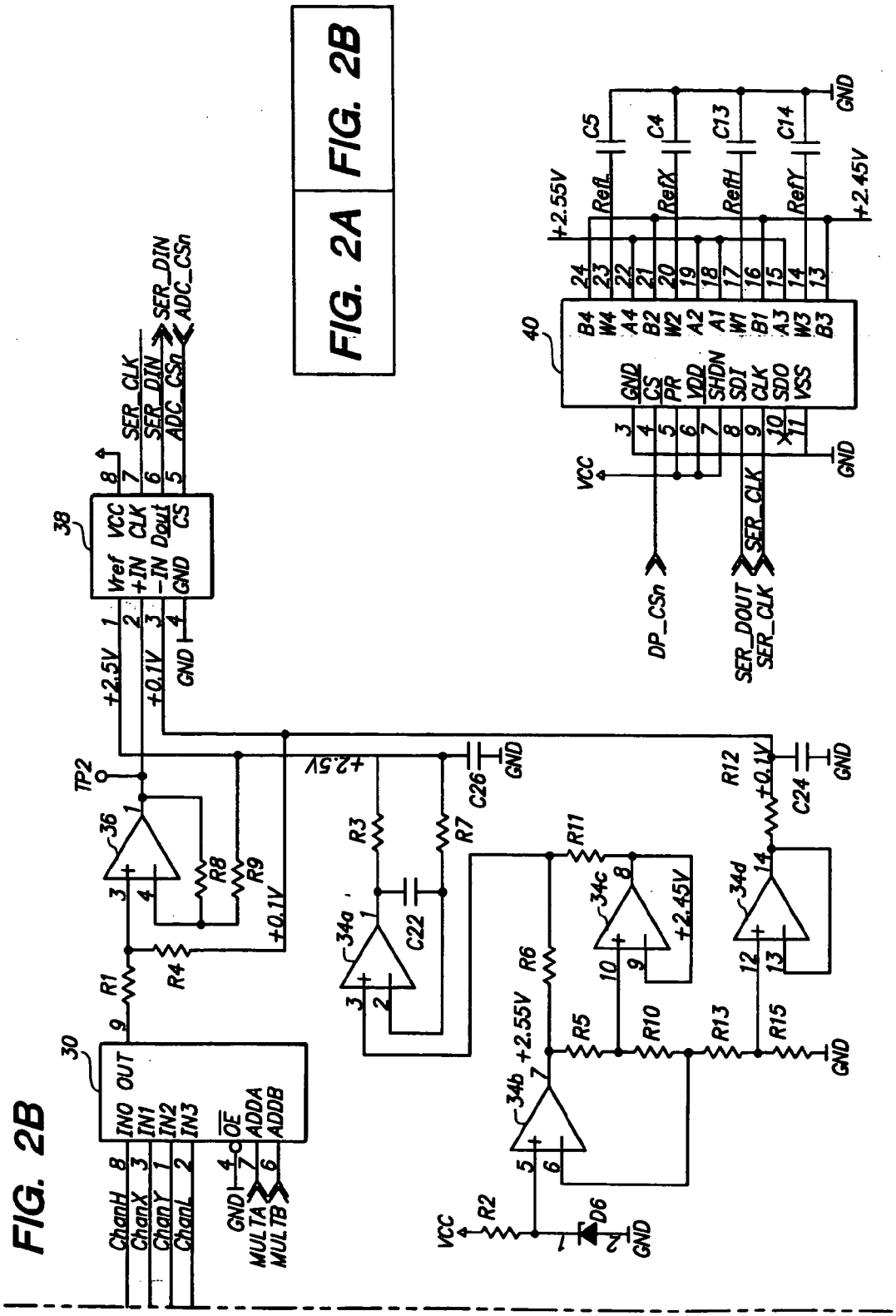
FIG. 2A
FIG. 2B
FIG. 2B
30
38
40
36
34a
34b
34c
34d
TP2
VCC
GND
+2.5V
+0.1V
+2.45V
+2.55V
ChanH
ChanX
ChanY
ChanL
MULTA
MULTB
IN0
IN1
IN2
IN3
OUT
OE
ADDA
ADDB
Vref
+IN
-IN
GND
VCC
CLK
Dout
CS
SER_CLK
SER_DIN
ADC_CSn
DP_CSn
SER_DOUT
SER_CLK
B4
W4
A4
B2
W2
A2
A1
W1
B1
A3
W3
B3
GND
CS
PR
VDD
SHDN
SDI
CLK
SDO
VSS
RefL
RefX
RefH
RefY
C5
C4
C13
C14
R1
R2
R3
R4
R5
R6
R7
R8
R9
R10
R11
R12
R13
R15
C22
C24
C26
D6