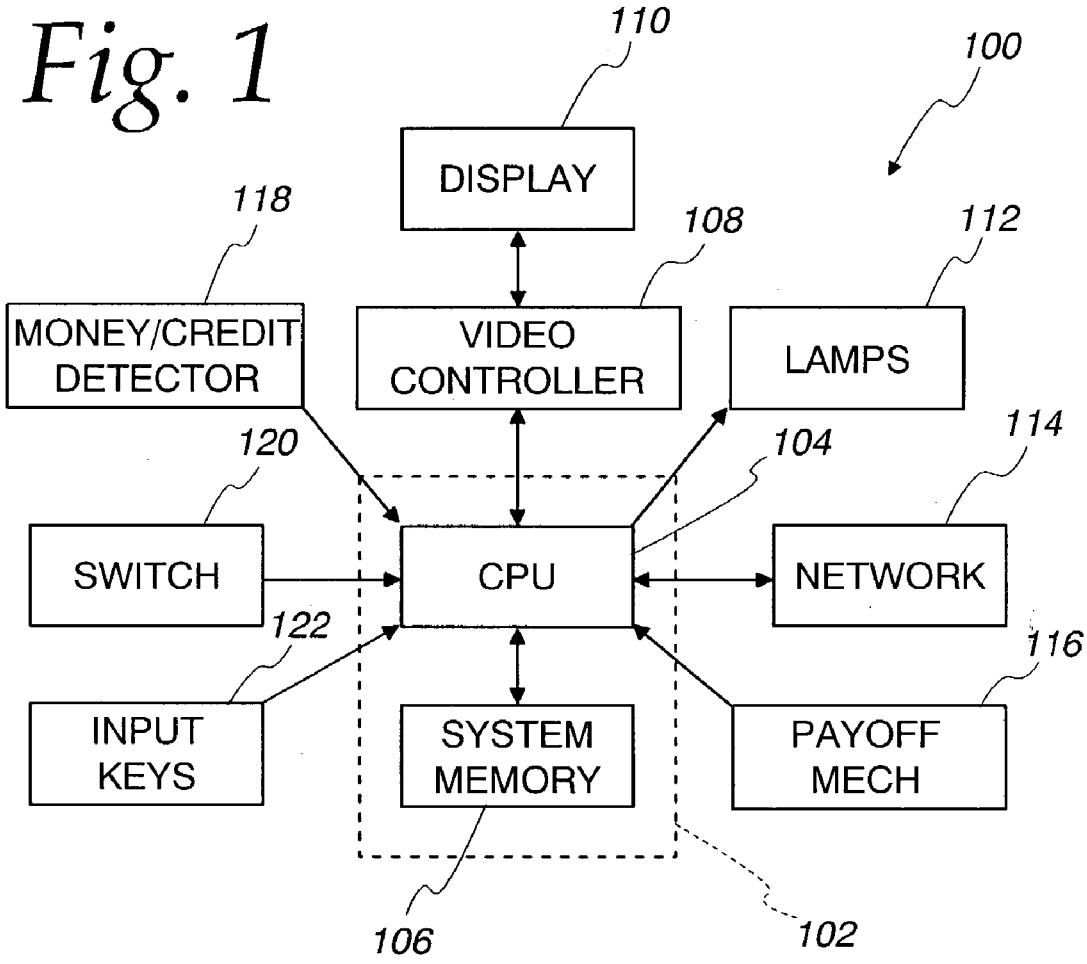
Fig. 1
110
100
DISPLAY
118
108
112
MONEY/CREDIT DETECTOR
VIDEO CONTROLLER
LAMPS
120
104
114
SWITCH
CPU
NETWORK
122
116
INPUT KEYS
SYSTEM MEMORY
PAYOFF MECH
106
102