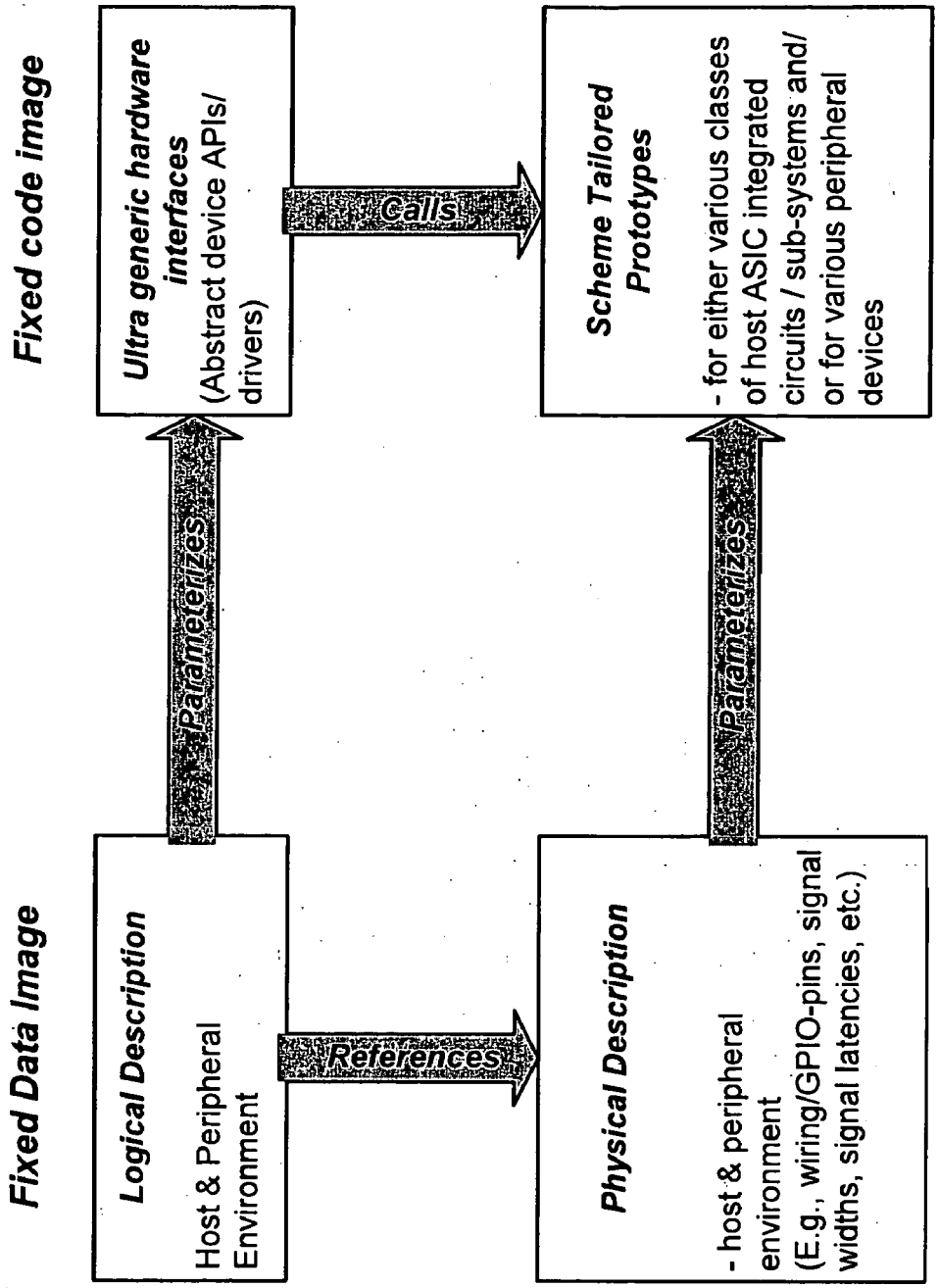
Fig. 1


Fixed Data Image
Fixed code image
Logical Description
Host & Peripheral Environment
Parameterizes
Ultra generic hardware interfaces
(Abstract device APIs/ drivers)
References
Calls
Physical Description
- host & peripheral environment
(E.g., wiring/GPIO-pins, signal widths, signal latencies, etc.)
Parameterizes
Scheme Tailored Prototypes
- for either various classes of host ASIC integrated circuits / sub-systems and/ or for various peripheral devices