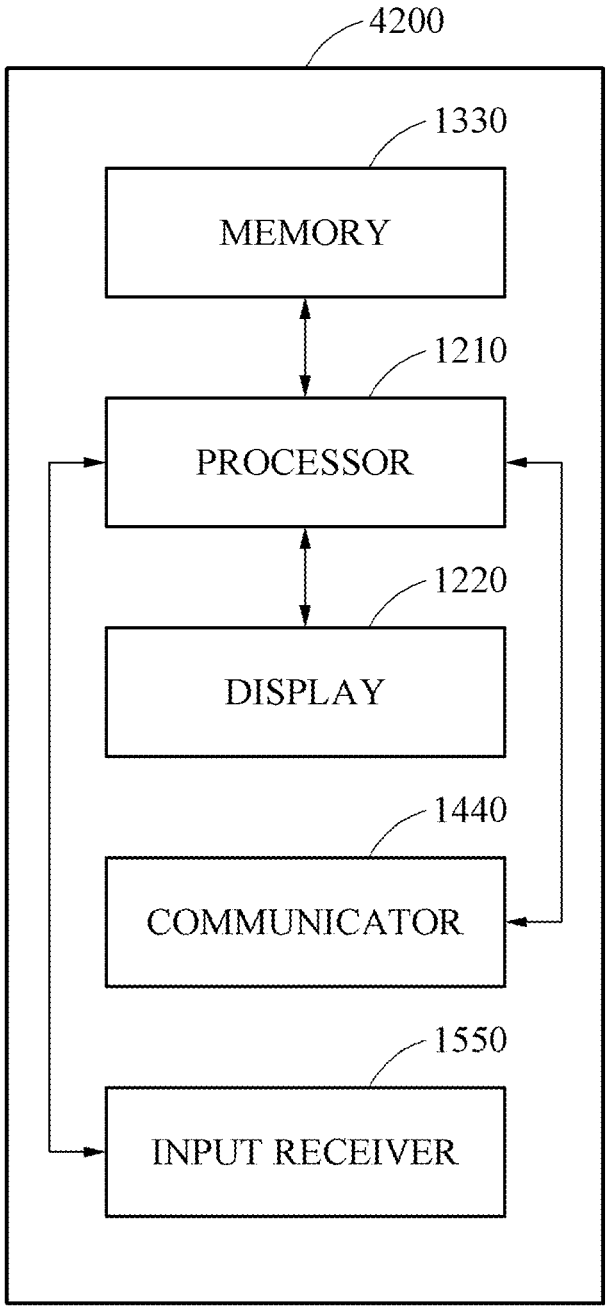
FIG. 15


4200
1330
MEMORY
1210
PROCESSOR
1220
DISPLAY
1440
COMMUNICATOR
1550
INPUT RECEIVER