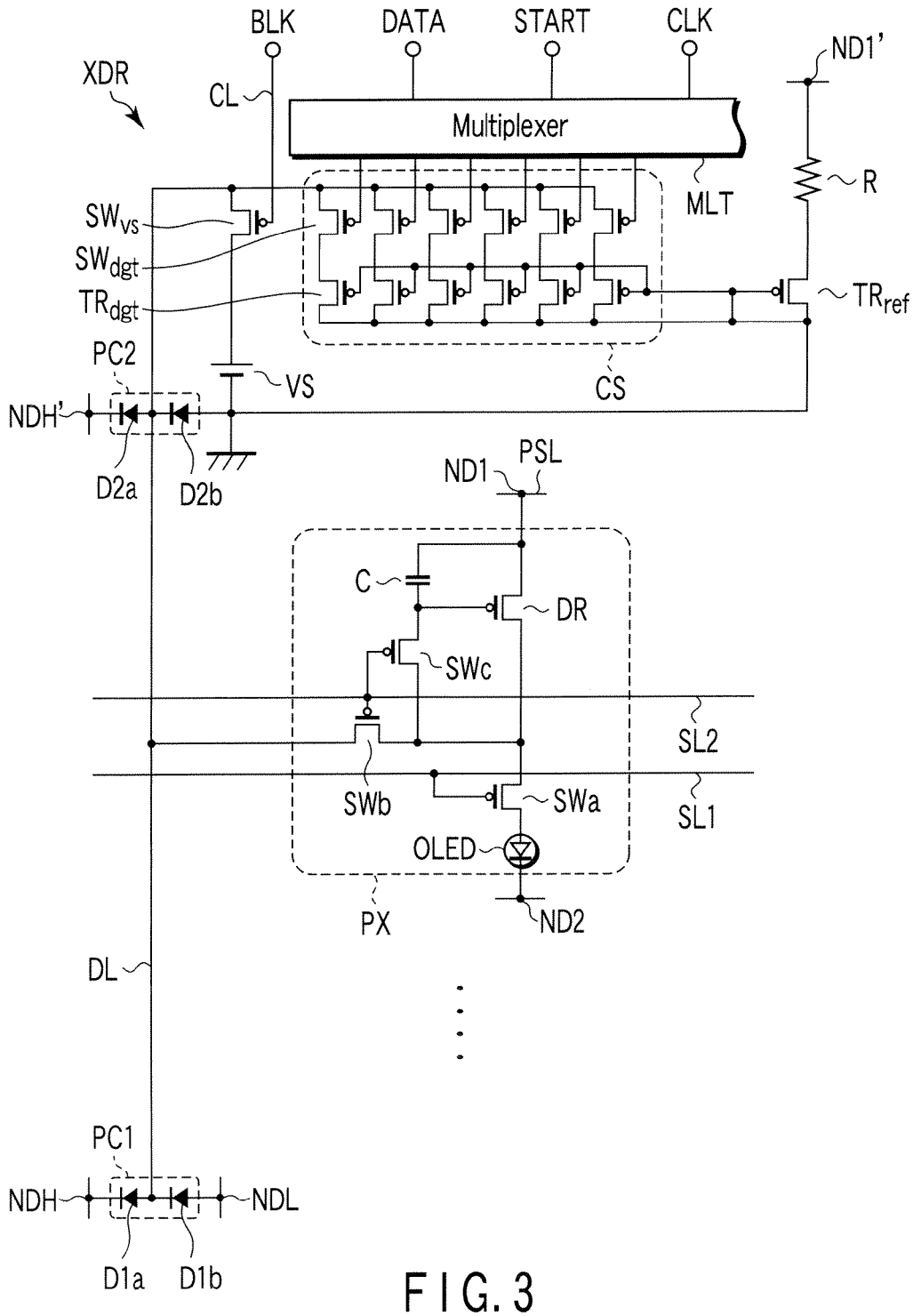
BLK
DATA
START
CLK
ND1'
XDR
CL
Multiplexer
R
MLT
SWvs
SWdgt
TRdgt
TRref
PC2
VS
CS
NDH'
D2a
D2b
ND1
PSL
C
DR
SWc
SL2
SWb
SWa
SL1
OLED
PX
ND2
DL
PC1
NDH
NDL
D1a
D1b

FIG. 3