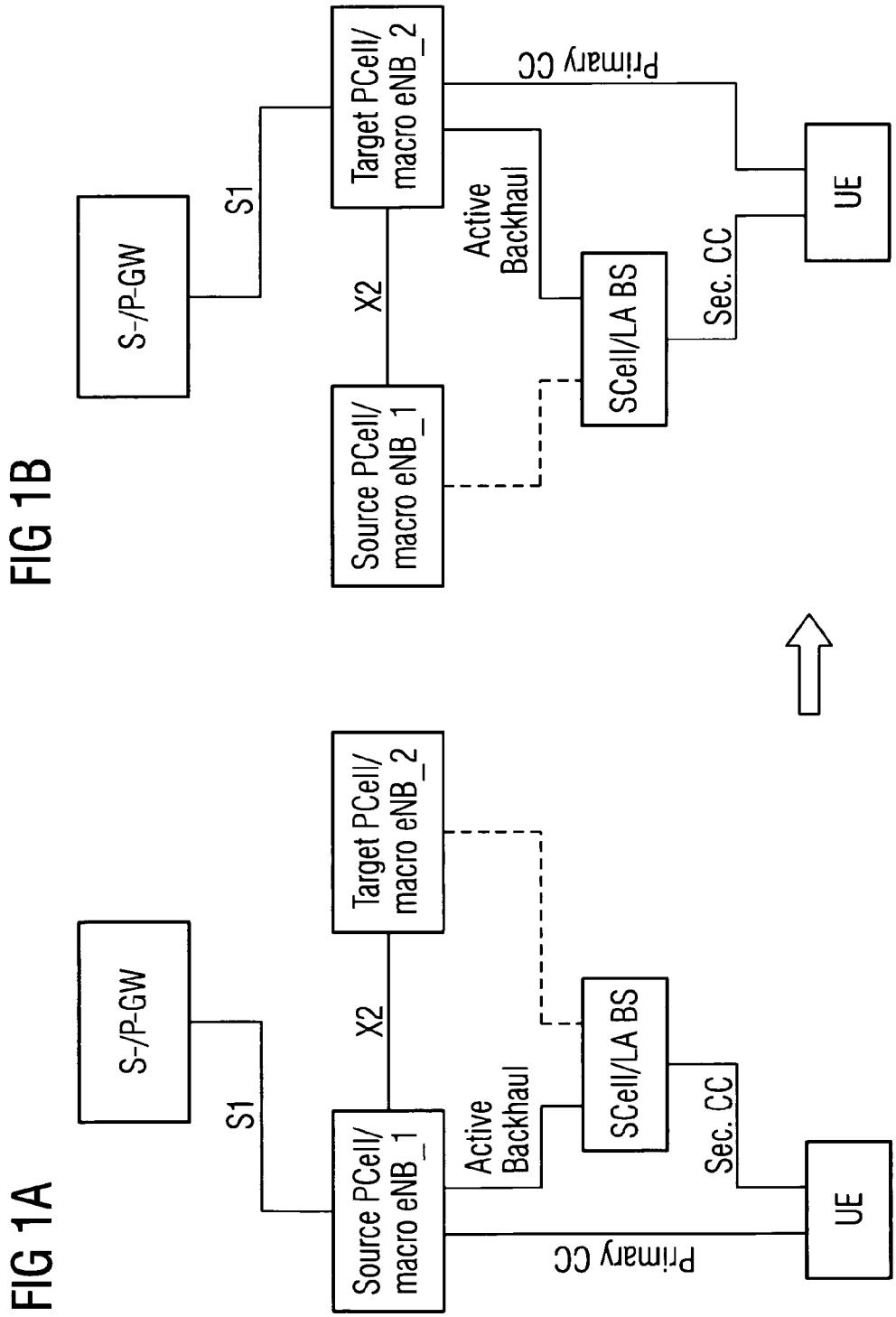
S-/P-GW
S1
Source PCell/
macro eNB_1
X2
Target PCell/
macro eNB_2
Active
Backhaul
SCell/LA BS
Sec. CC
UE
Primary CC
S-/P-GW
S1
Target PCell/
macro eNB_2
Source PCell/
macro eNB_1
X2
Active
Backhaul
SCell/LA BS
Sec. CC
UE
Primary CC

FIG 1A

FIG 1B