FIG. 11

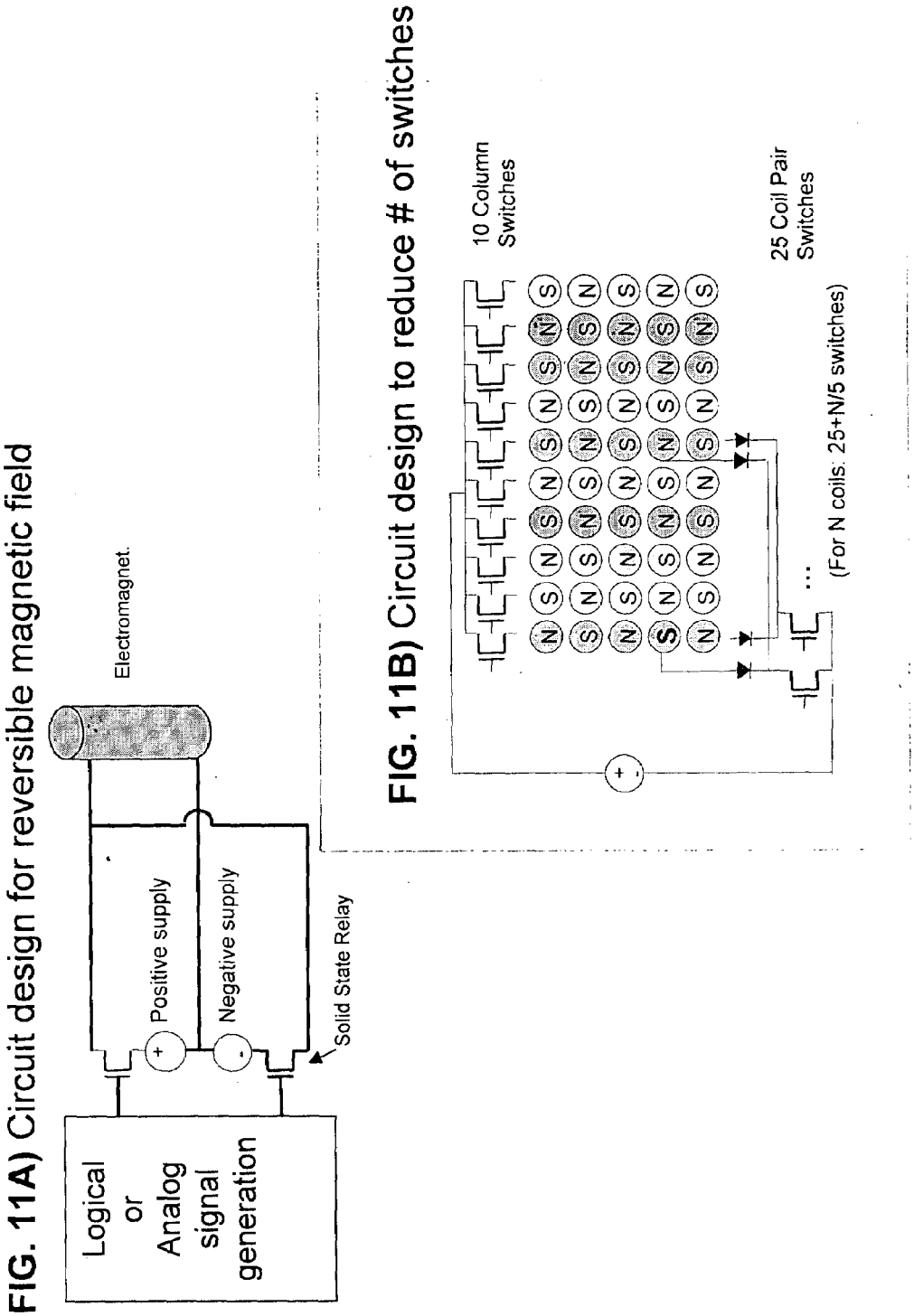

FIG. 11A) Circuit design for reversible magnetic field

FIG. 11B) Circuit design to reduce # of switches