FIG. 4

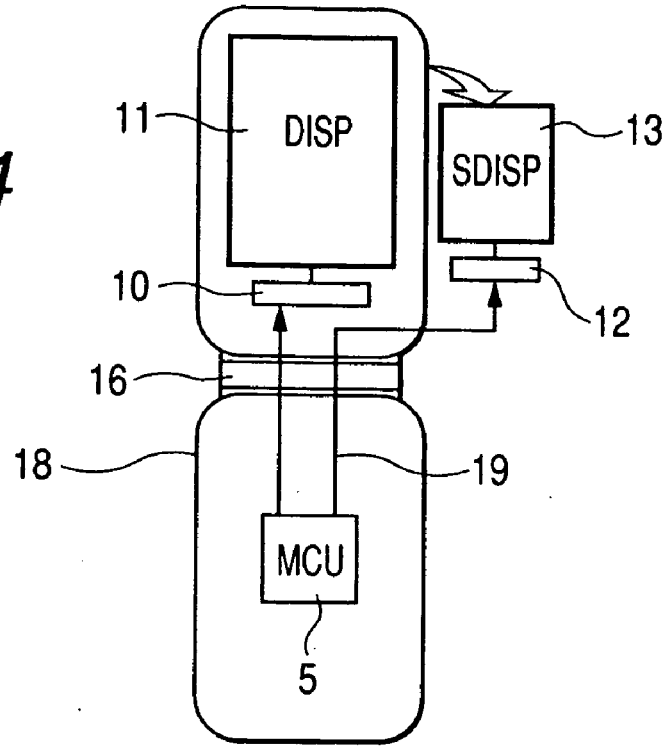
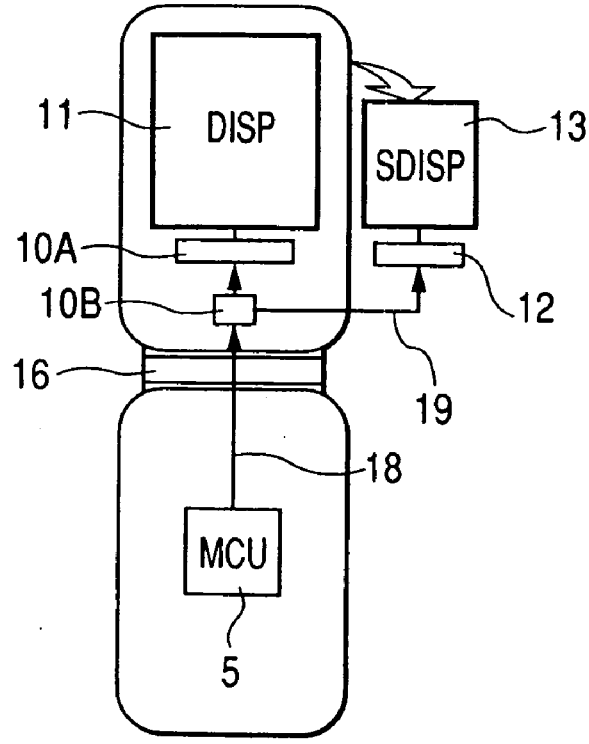

11
DISP
SDISP
13
10
12
16
18
19
MCU
5

FIG. 5


11
DISP
SDISP
13
10A
10B
12
16
19
18
MCU
5