FIG. 1

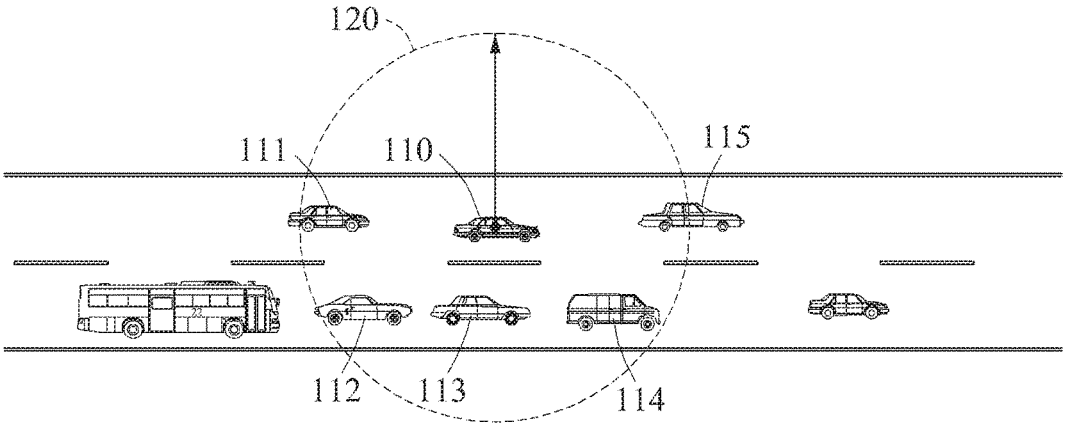
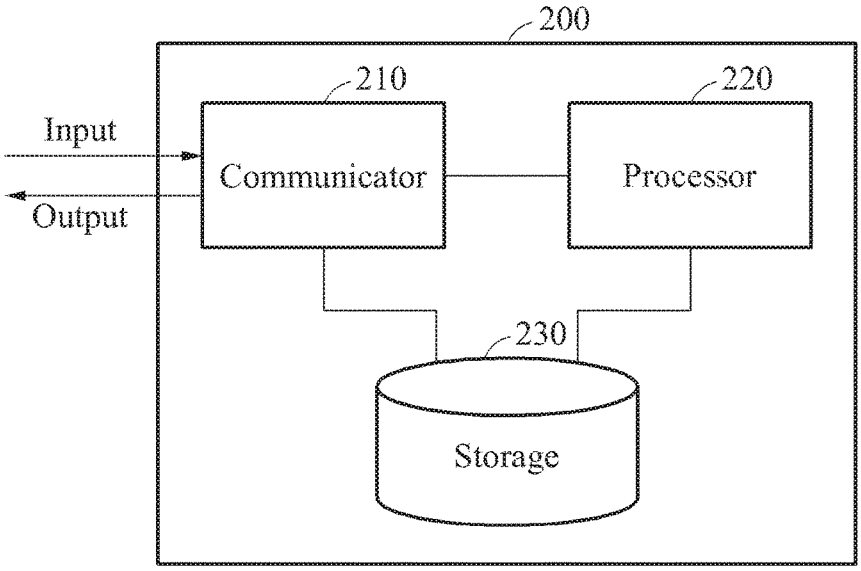

120
111
110
115
112
113
114

FIG. 2


200
210
220
Input
Output
Communicator
Processor
230
Storage