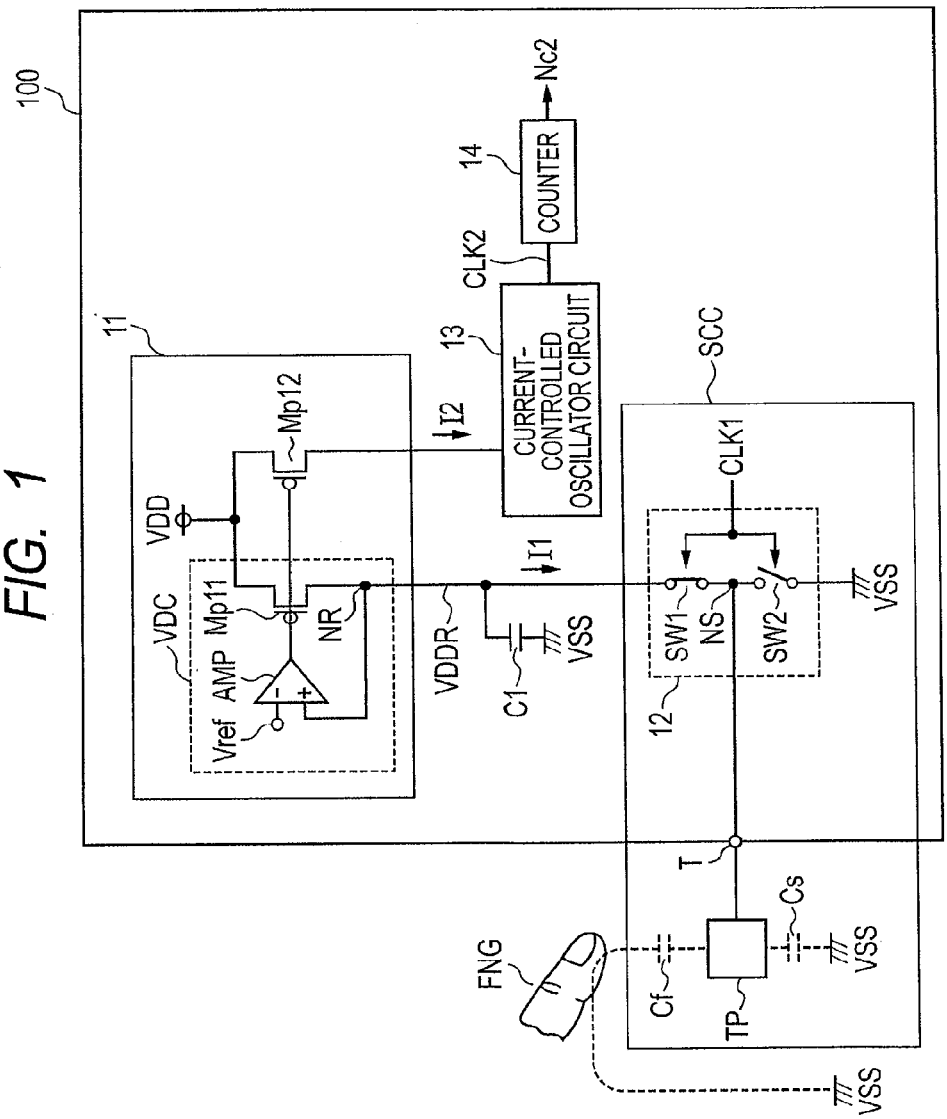
FIG. 1


100
11
VDD
Mp12
VDC
Mp11
AMP
Vref
NR
VDDR
C1
VSS
I1
I2
13
CURRENT-CONTROLLED OSCILLATOR CIRCUIT
CLK2
14
COUNTER
Nc2
SCC
CLK1
SW1
NS
SW2
12
T
Cs
Cf
TP
FNG