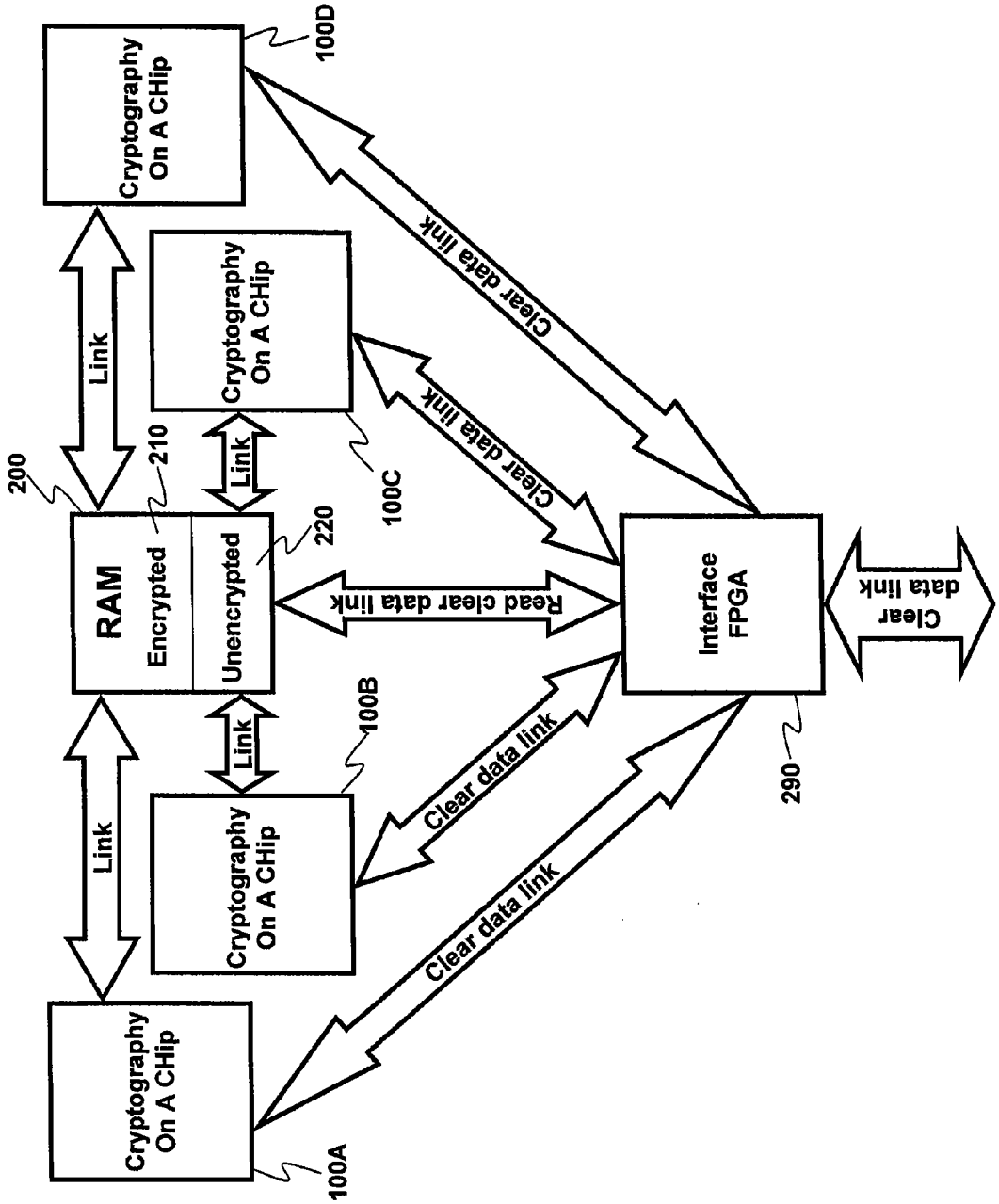
Cryptography On A CHip
100D
Link
Cryptography On A CHip
Link
200
210
RAM
Encrypted
Unencrypted
220
Link
100C
Clear data link
Clear data link
Read clear data link
Interface FPGA
Clear data link
Link
Cryptography On A CHip
100B
Clear data link
Clear data link
290
Cryptography On A CHip
100A

Figure 3