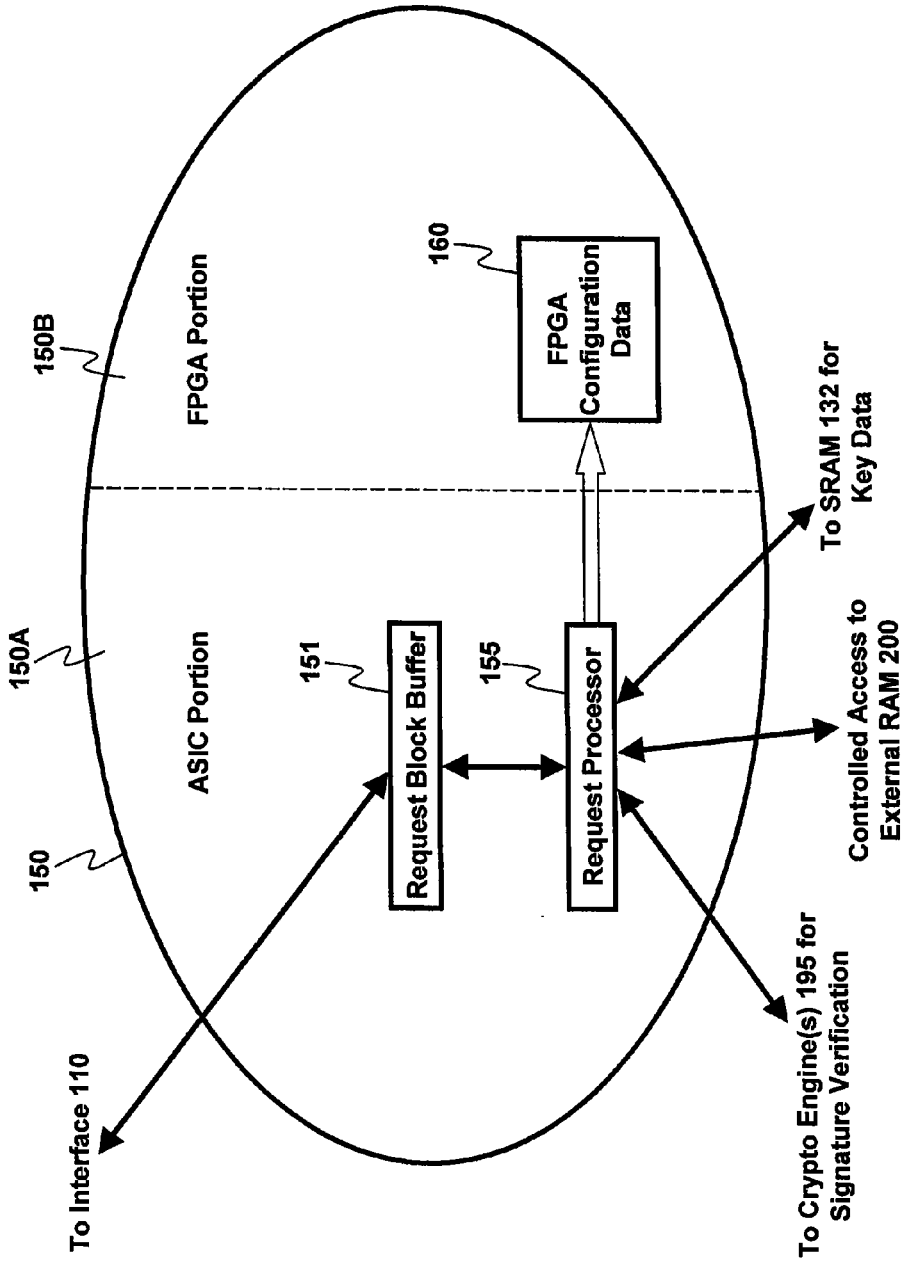
150
150A
150B
ASIC Portion
FPGA Portion
151
Request Block Buffer
155
Request Processor
160
FPGA Configuration Data
To Interface 110
To SRAM 132 for Key Data
Controlled Access to External RAM 200
To Crypto Engine(s) 195 for Signature Verification

Figure 5