Fig.14

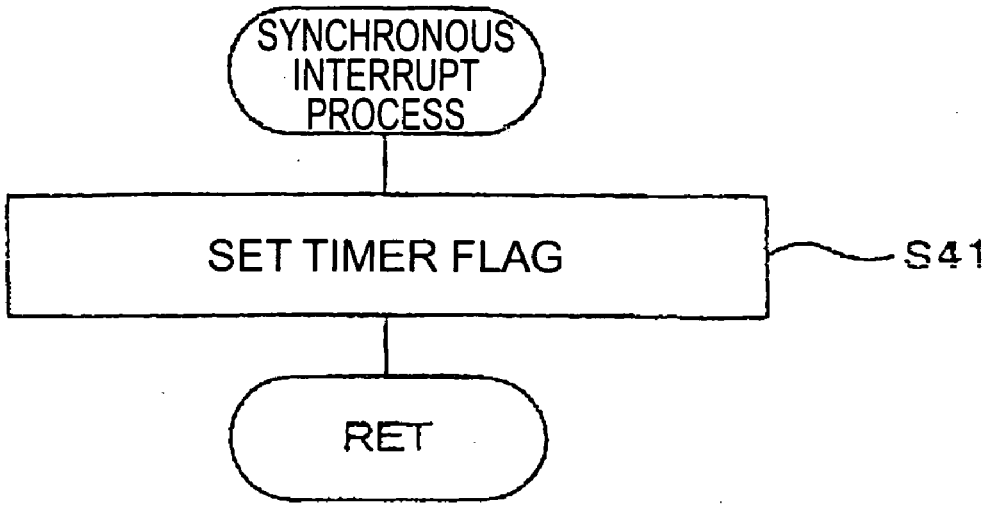
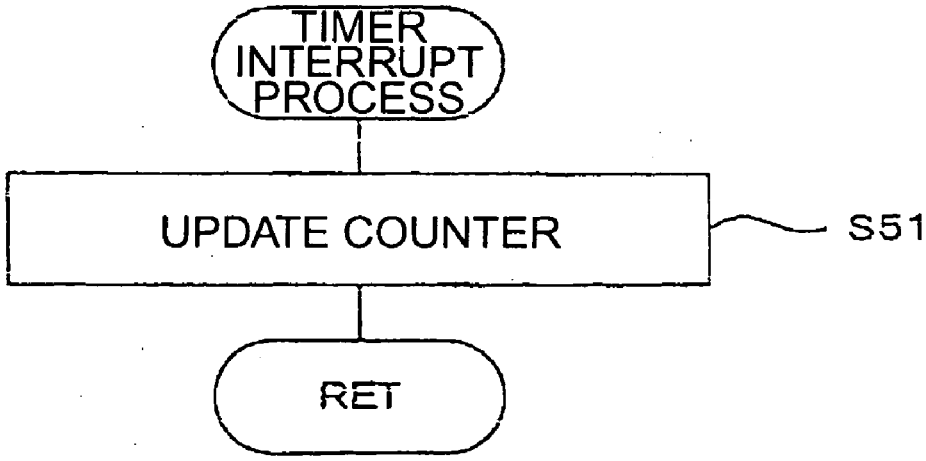

SYNCHRONOUS INTERRUPT PROCESS
SET TIMER FLAG
S41
RET

Fig.15


TIMER INTERRUPT PROCESS
UPDATE COUNTER
S51
RET