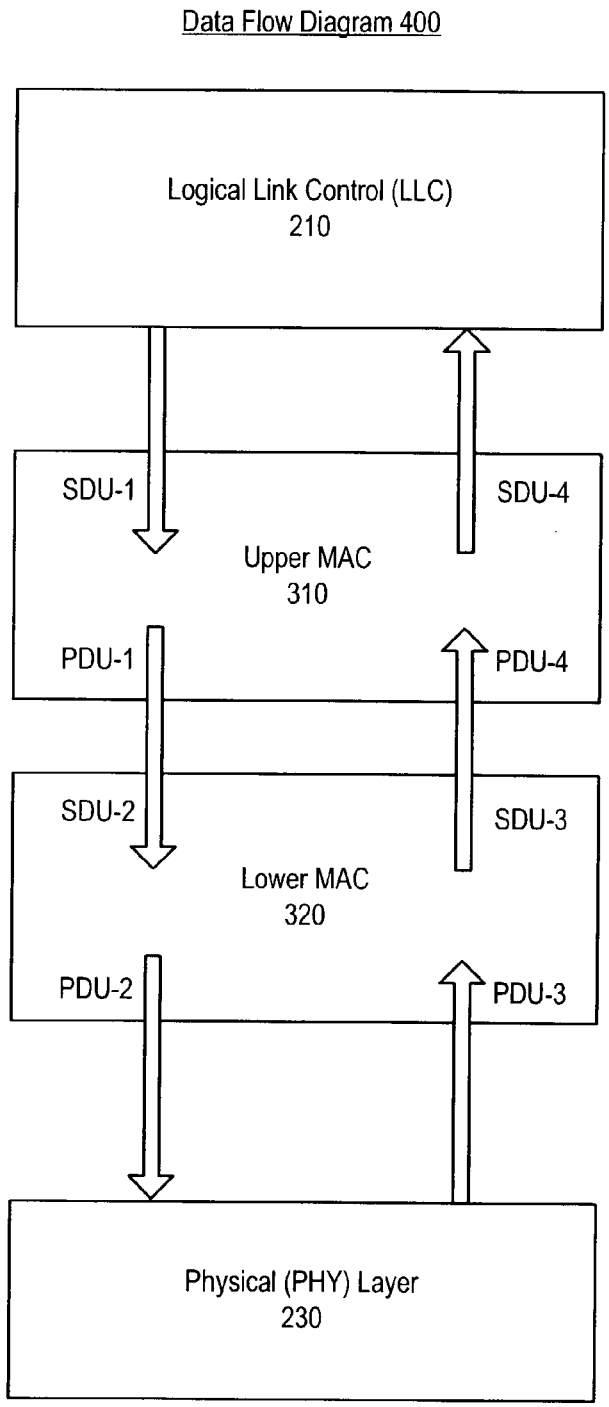
FIG. 4


Data Flow Diagram 400
Logical Link Control (LLC)
210
SDU-1
SDU-4
Upper MAC
310
PDU-1
PDU-4
SDU-2
SDU-3
Lower MAC
320
PDU-2
PDU-3
Physical (PHY) Layer
230