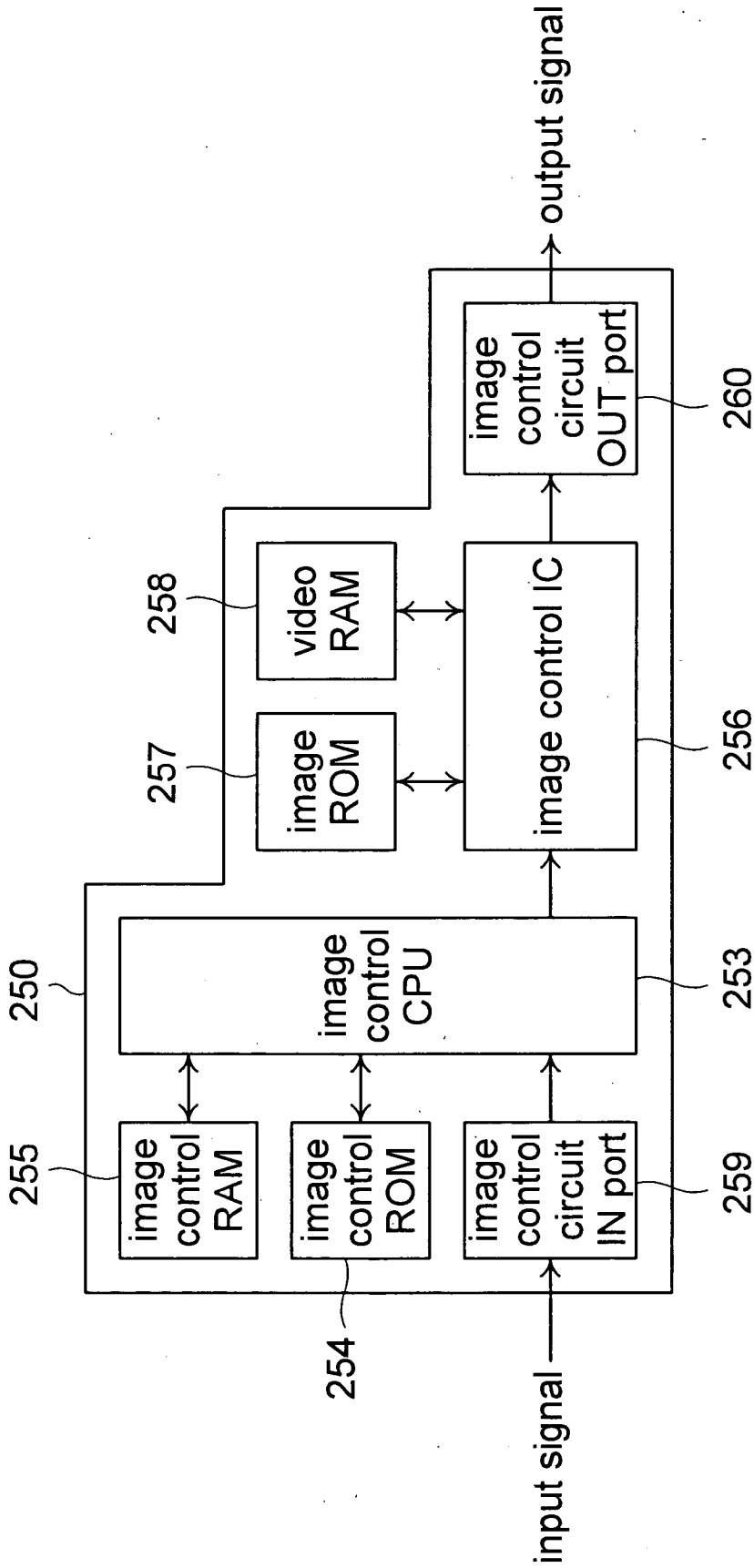
Fig. 5


250
255
image control RAM
image control ROM
254
image control CPU
253
image control circuit IN port
259
input signal
image ROM
257
video RAM
258
image control IC
256
image control circuit OUT port
260
output signal