FIG. 1

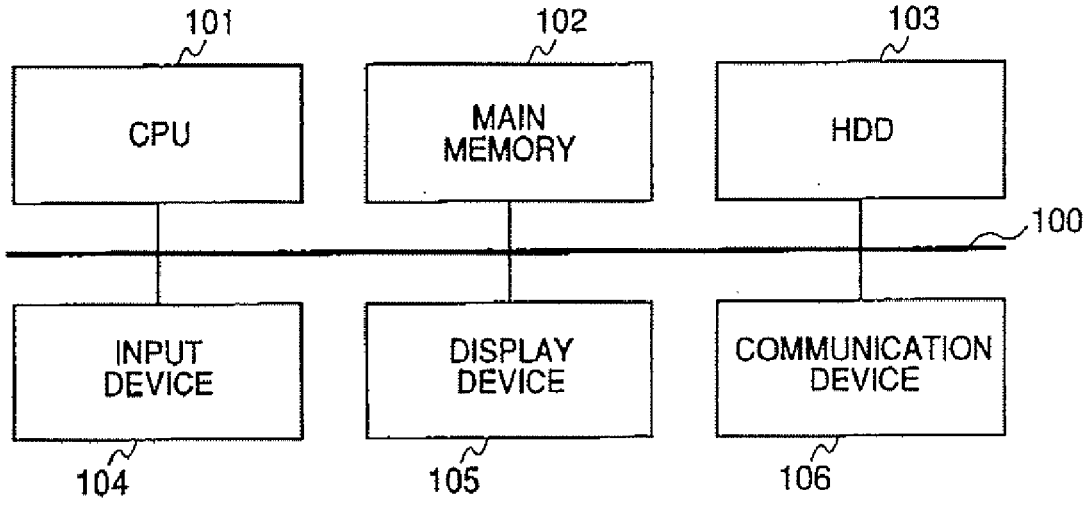
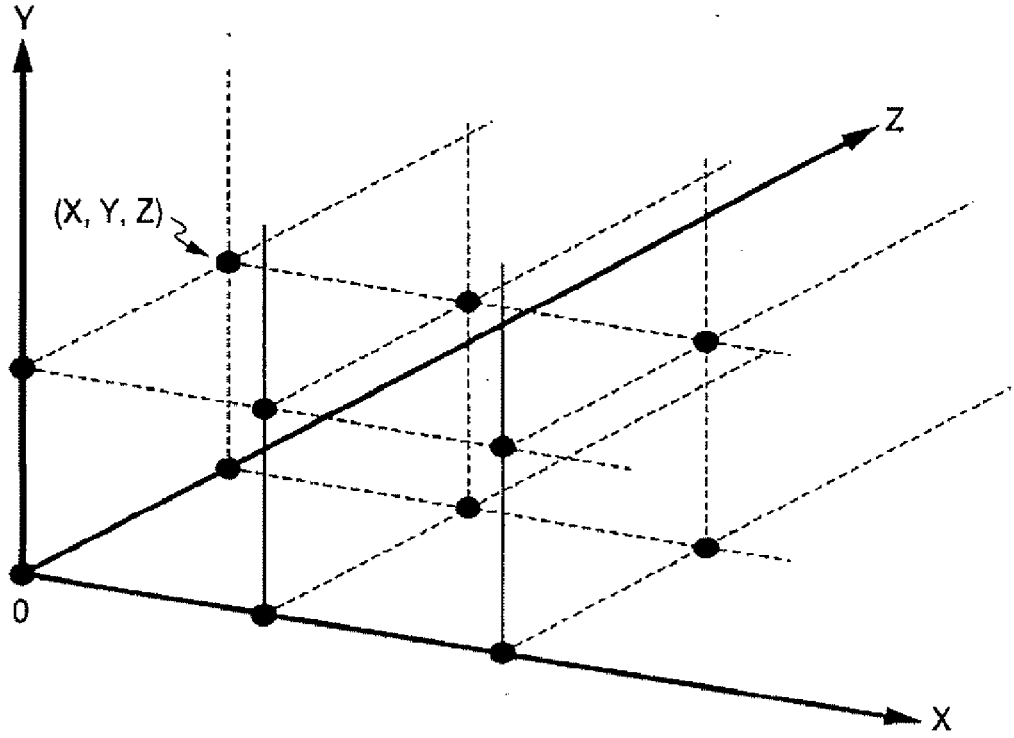

101
102
103
CPU
MAIN MEMORY
HDD
100
INPUT DEVICE
DISPLAY DEVICE
COMMUNICATION DEVICE
104
105
106

FIG. 2


Y
Z
(X, Y, Z)
0
X