FIG. 1

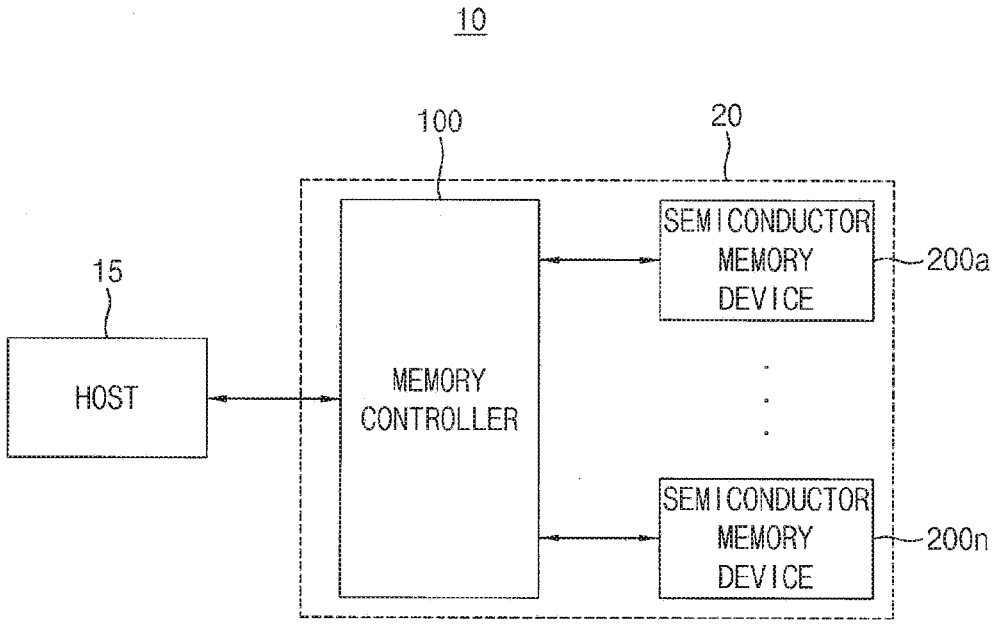
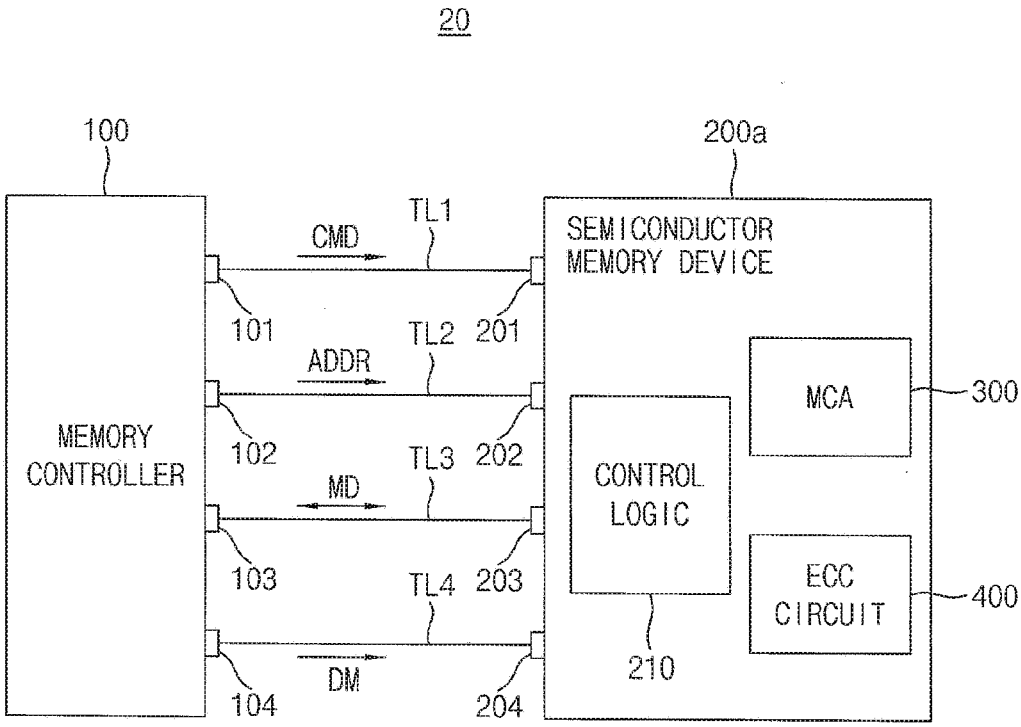

10
100
20
15
HOST
MEMORY CONTROLLER
SEMICONDUCTOR MEMORY DEVICE
200a
SEMICONDUCTOR MEMORY DEVICE
200n

FIG. 2


20
100
200a
MEMORY CONTROLLER
CMD
TL1
101
201
ADDR
TL2
102
202
MD
TL3
103
203
TL4
DM
104
204
SEMICONDUCTOR MEMORY DEVICE
CONTROL LOGIC
210
MCA
300
ECC CIRCUIT
400