FIG. 12

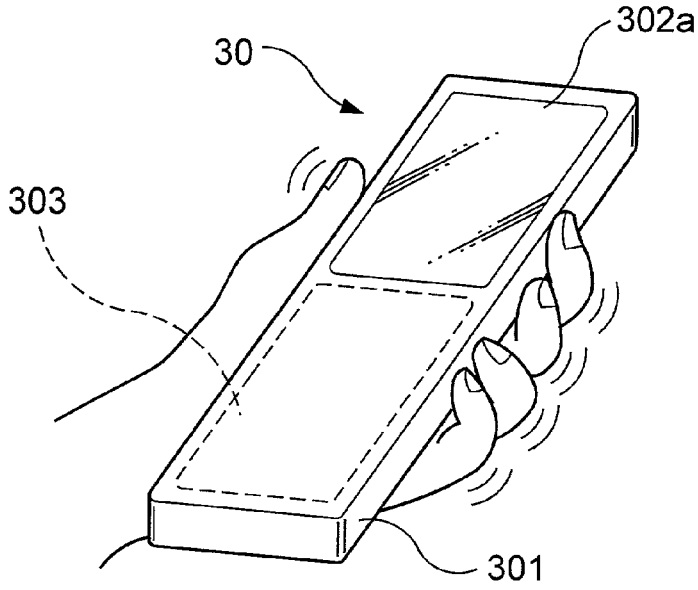
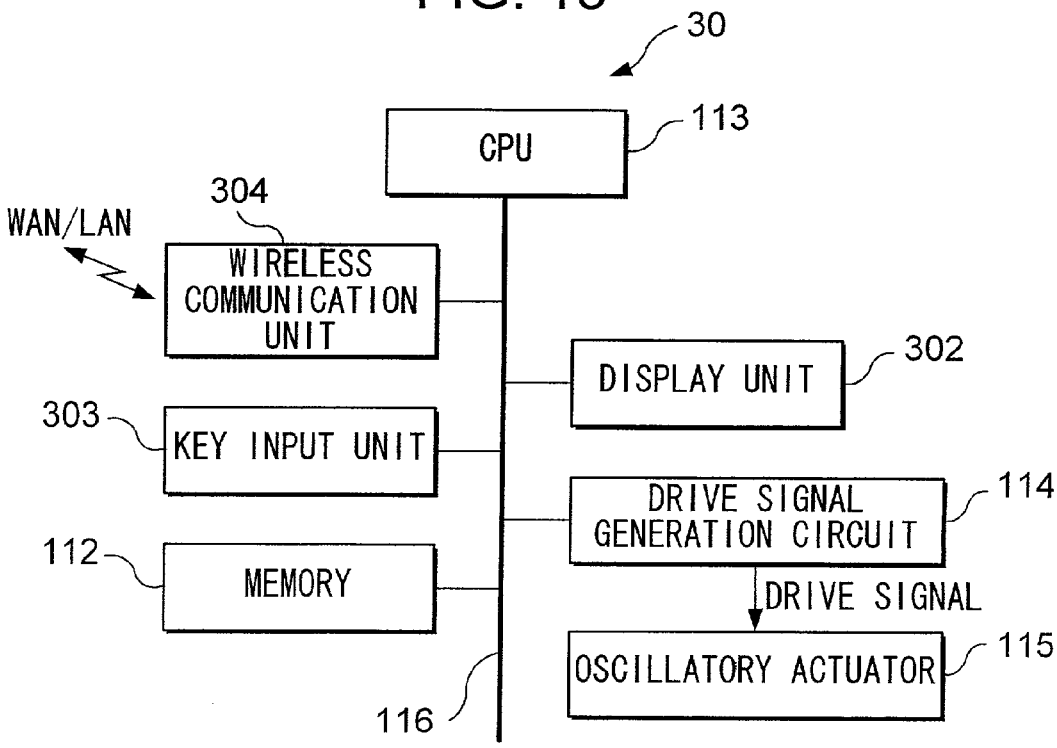

30
302a
303
301

FIG. 13


30
CPU
113
WAN/LAN
304
WIRELESS COMMUNICATION UNIT
DISPLAY UNIT
302
303
KEY INPUT UNIT
DRIVE SIGNAL GENERATION CIRCUIT
114
112
MEMORY
DRIVE SIGNAL
OSCILLATORY ACTUATOR
115
116