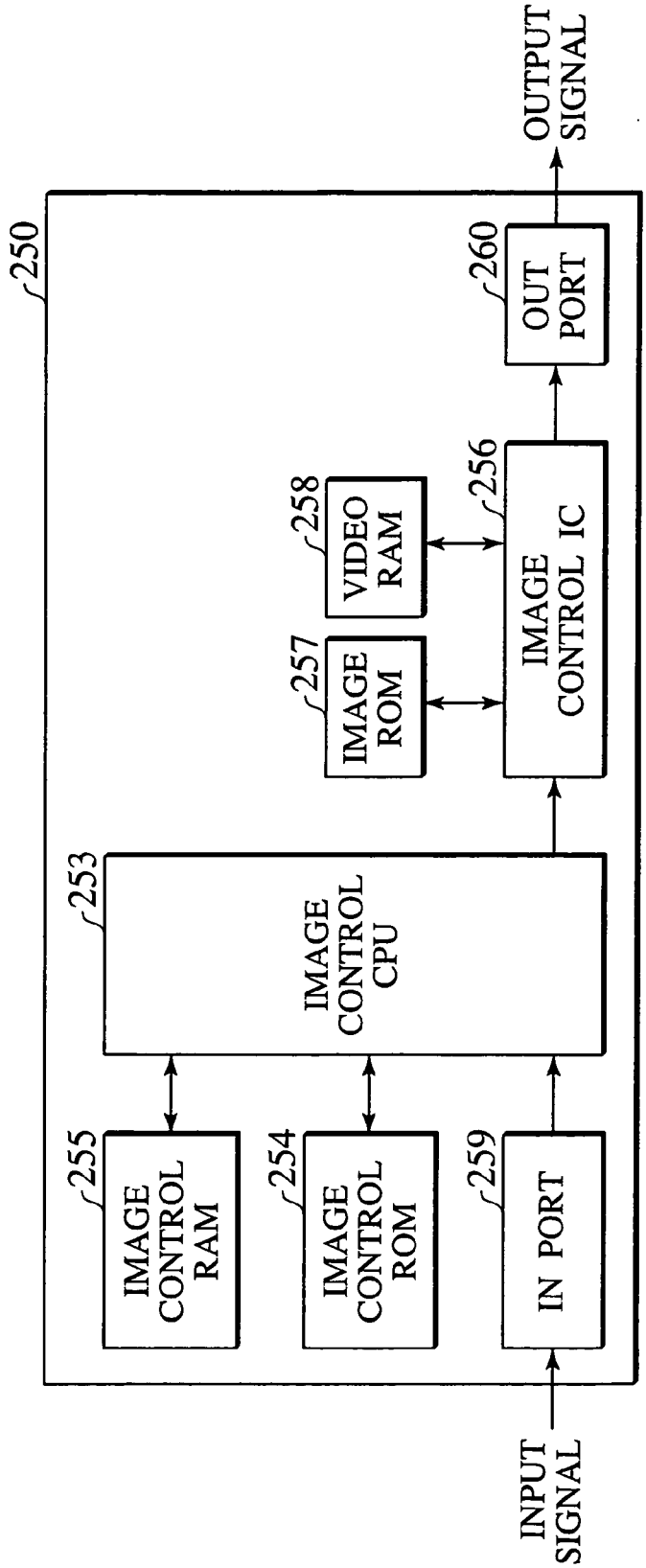
FIG.5


250
255
IMAGE CONTROL RAM
253
IMAGE CONTROL CPU
254
IMAGE CONTROL ROM
259
IN PORT
INPUT SIGNAL
257
IMAGE ROM
258
VIDEO RAM
256
IMAGE CONTROL IC
260
OUT PORT
OUTPUT SIGNAL