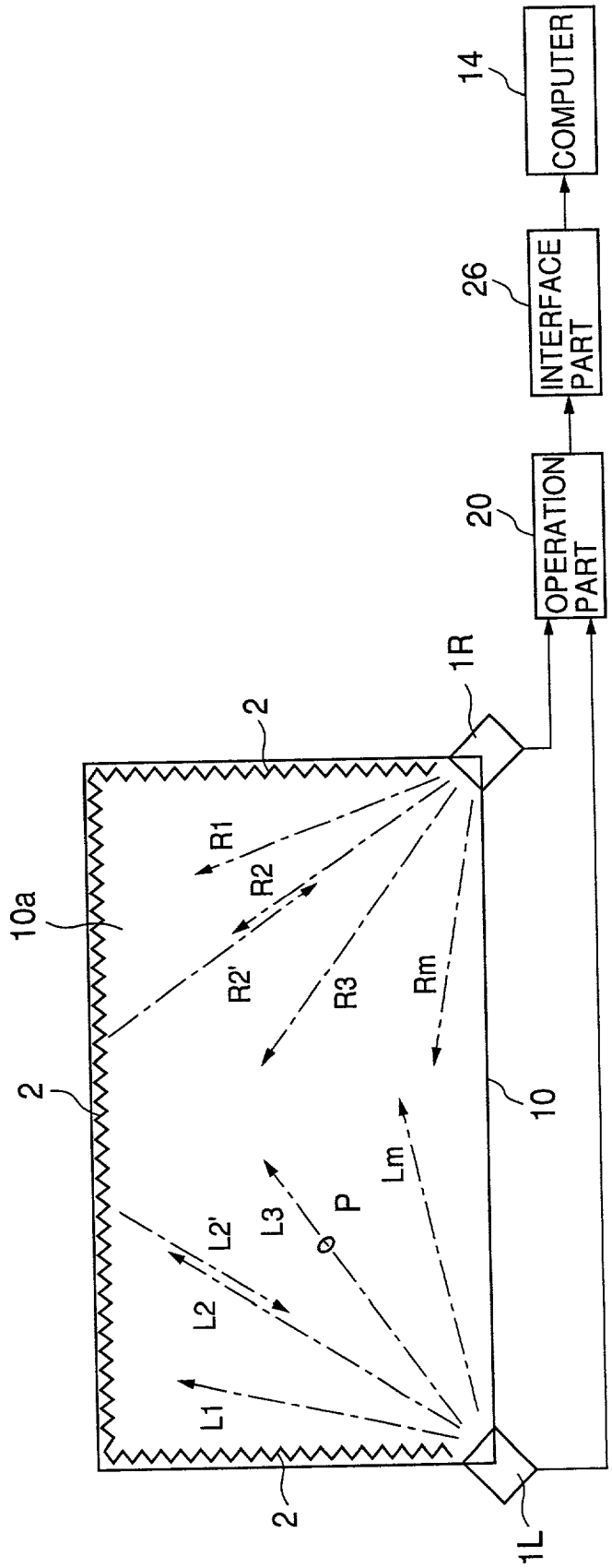
FIG.3


COMPUTER
14
INTERFACE PART
26
OPERATION PART
20
1R
2
R1
R2
R2'
R3
Rm
10a
2
10
Lm
P
L3
L2'
L2
L1
2
1L