FIG. 1

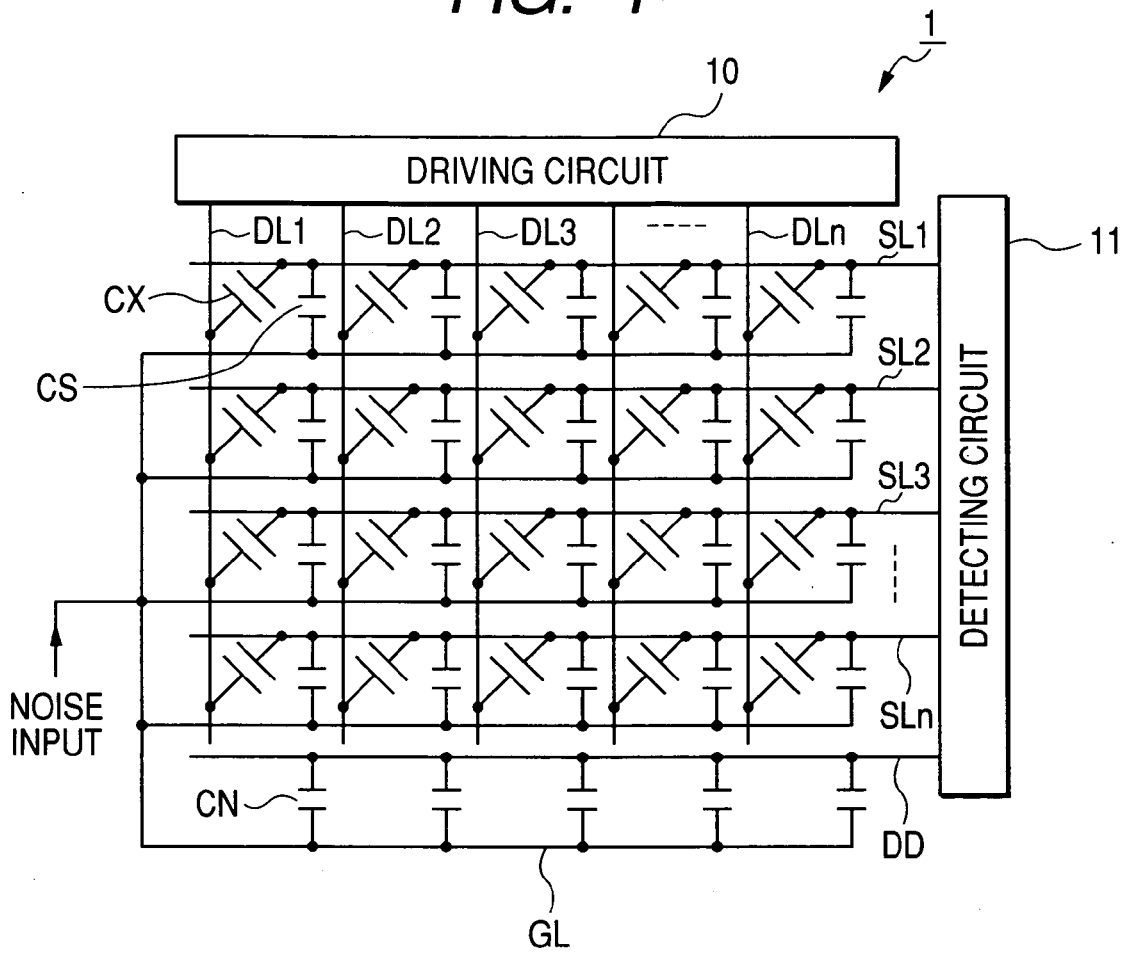
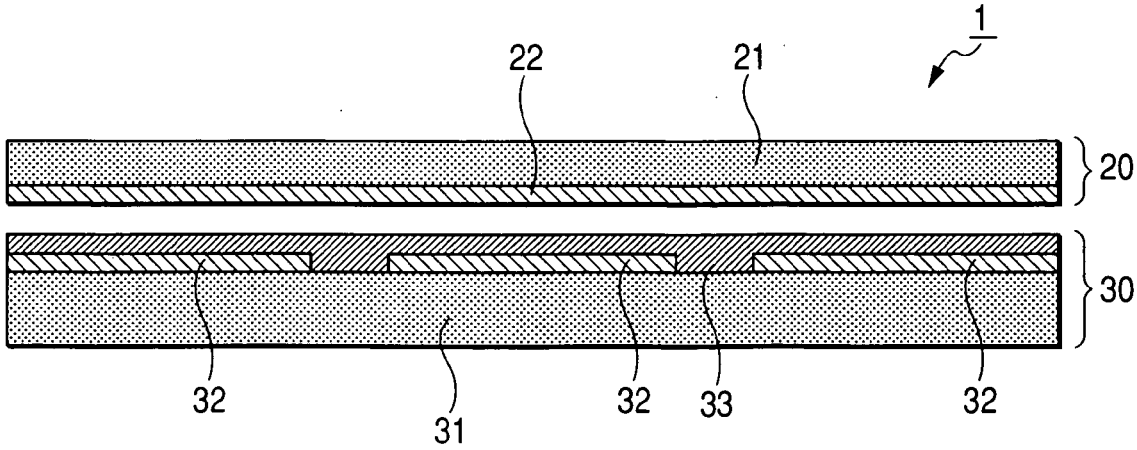

1
10
DRIVING CIRCUIT
DL1
DL2
DL3
DLn
SL1
11
CX
CS
SL2
SL3
DETECTING CIRCUIT
NOISE INPUT
SLn
CN
DD
GL

FIG. 2


1
22
21
20
30
32
31
32
33
32