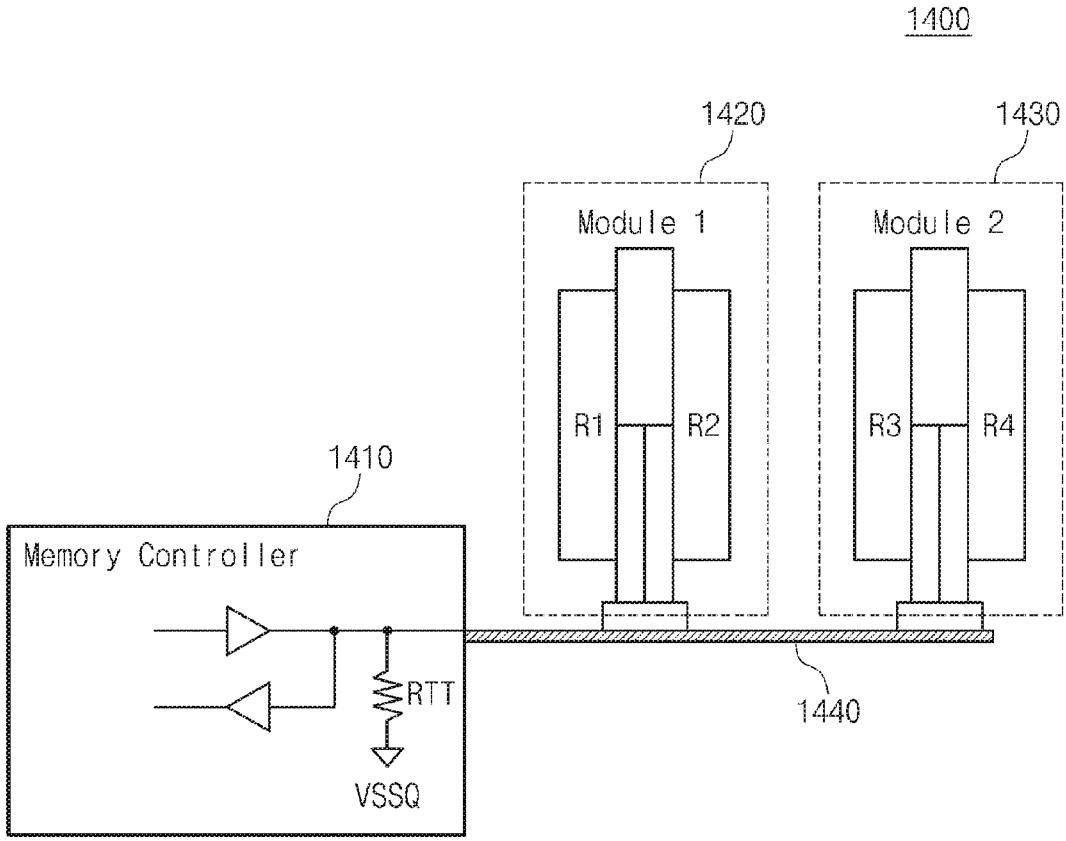
FIG. 13


1400
1420
1430
Module 1
Module 2
R1
R2
R3
R4
1410
Memory Controller
RTT
VSSQ
1440