

9. The driving circuit according to claim 1, wherein the source of the reference transistor is provided with the second potential.

10. The driving circuit according to claim 9, wherein the second potential is substantially a grounded potential.

11. A driving circuit of a current-driven active matrix organic light emitting diode (AMOLED) display, comprising:

- a plurality of scan lines;
- a plurality of data lines;
- a plurality of pixel driving units, each pixel driving unit comprising:
 - a switch transistor, having a source electrode connected to one of the data lines and a gate electrode connected to one of the scan lines;
 - a driving transistor, having a gate electrode connected to a drain electrode of the switch transistor, and a drain electrode provided with a first potential; and
 - a displaying organic light emitting diode (OLED), having an anode connected to a source electrode of the driving transistor and a cathode provided with a second potential; and
- a plurality of reference units, electrically connected to the pixel driving units through the data lines, each reference unit comprising:

- a reference OLED, corresponding to the displaying OLED, having an anode connected to the data line, and a cathode provided with the second potential.

12. The driving circuit according to claim 11, further comprising a capacitor, having an end electronically connected to both of the source electrode of the switch transistor and the gate electrode of the driving transistor.

13. The driving circuit according to claim 12, wherein the capacitor has an opposite end connected to the drain electrode of the driving transistor.

14. The driving circuit according to claim 11, wherein the second potential is substantially a grounded potential.

15. An active matrix organic light emitting diode (AMOLED) display panel comprising:

- a substrate;

- a plurality of scan lines disposed on the substrate;

- a plurality of data lines disposed on the substrate;

- a plurality of pixel driving units disposed on the substrate, each pixel driving unit comprising:

- a switch transistor, having a source electrode connected to one of the data lines and a gate electrode connected to one of the scan lines;

- a driving transistor, having a gate electrode connected to a drain electrode of the switch transistor, and a drain electrode provided with a first potential; and

- a displaying organic light emitting diode (OLED), having an anode connected to a source electrode of the driving transistor and a cathode provided with a second potential;

- a plurality of power line connected to the driving transistor for applying the first potential; and

- a plurality of reference units disposed on the substrate, electrically connected to the pixel driving units through the data lines, each reference unit comprising:

- a reference transistor, corresponding to the driving transistor, having a gate electrode, a drain electrode and a source electrode, the gate electrode and the drain electrode being connected to the data line, and

- a reference OLED, corresponding to the displaying OLED, having an anode connected to the source electrode of the reference transistor, and a cathode provided with the second potential.

16. The AMOLED display panel according to claim 15, wherein the pixel driving unit further comprising a capacitor, having an end electronically connected to both of the source electrode of the switch transistor and the gate electrode of the driving transistor.

17. The AMOLED display panel according to claim 16, wherein the capacitor has an opposite end connected to the drain electrode of the driving transistor.

18. The AMOLED display panel according to claim 15, wherein the second potential is substantially a grounded potential.

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