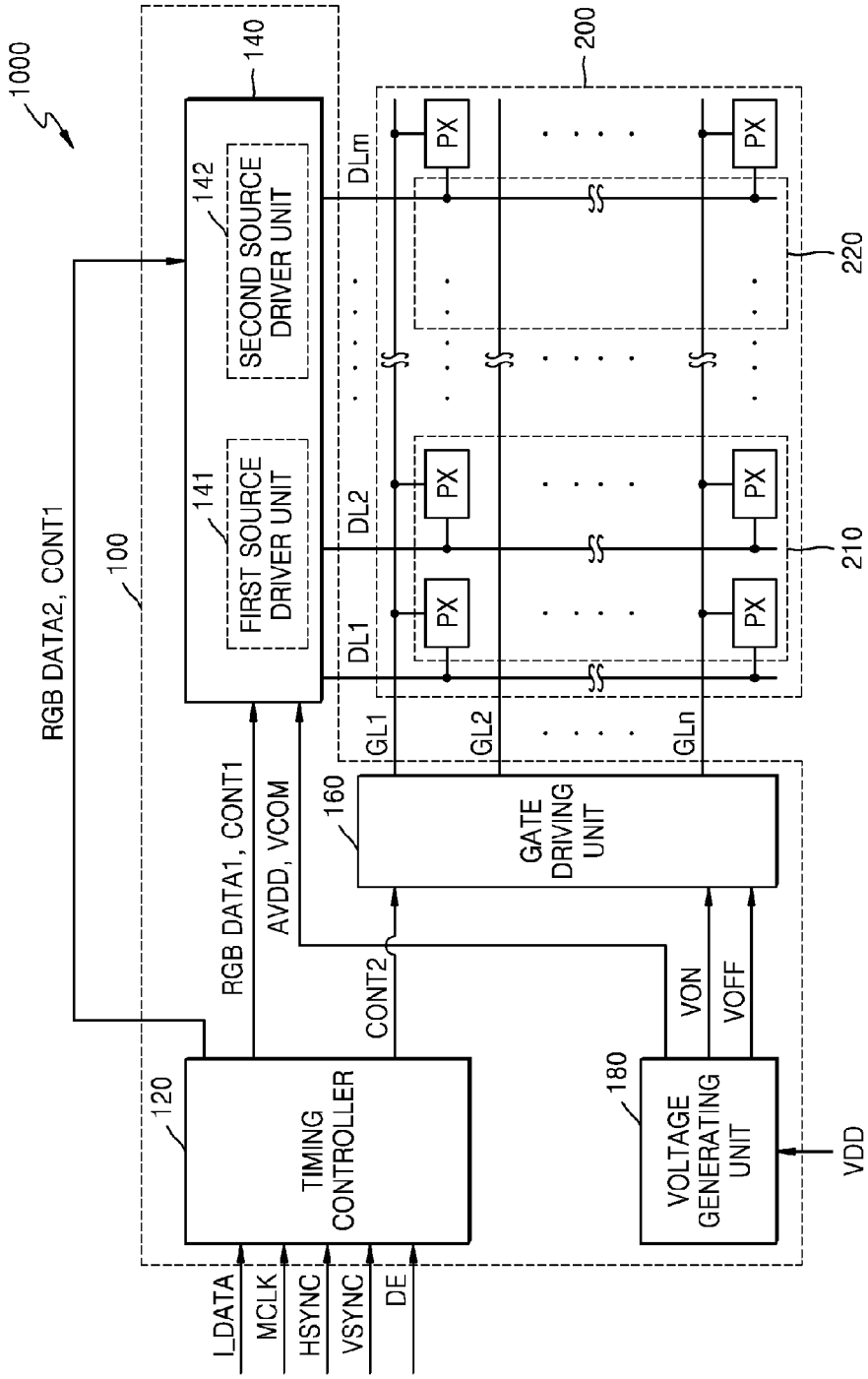
FIG. 1


1000
100
RGB DATA2, CONT1
140
142
SECOND SOURCE DRIVER UNIT
141
FIRST SOURCE DRIVER UNIT
DLm
DL2
DL1
200
PX
220
210
GL1
GL2
GLn
160
GATE DRIVING UNIT
RGB DATA1, CONT1
AVDD, VCOM
CONT2
VON
VOFF
120
TIMING CONTROLLER
180
VOLTAGE GENERATING UNIT
VDD
I_DATA
MCLK
HSYNC
VSYNC
DE