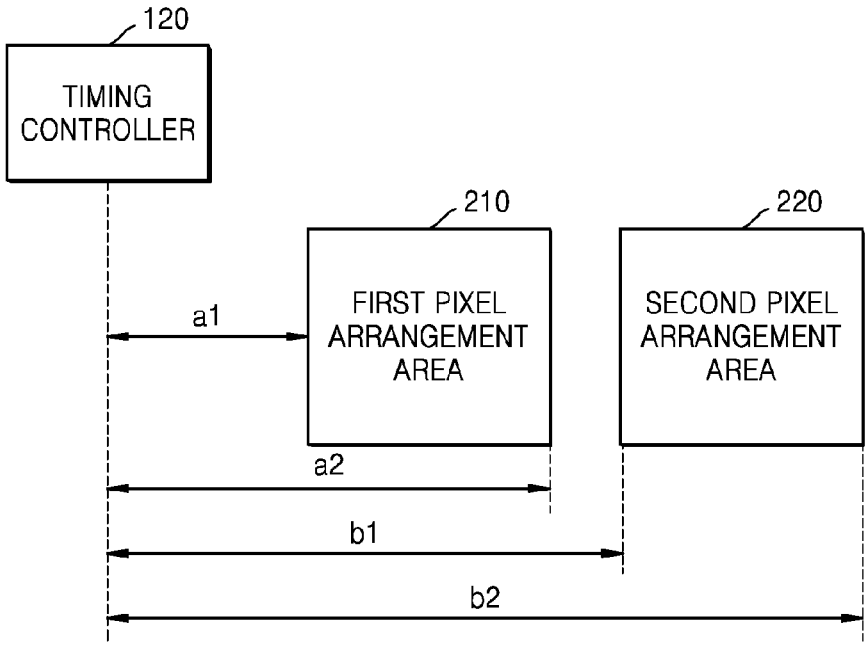
FIG. 2


120
TIMING CONTROLLER
210
FIRST PIXEL ARRANGEMENT AREA
220
SECOND PIXEL ARRANGEMENT AREA
a1
a2
b1
b2