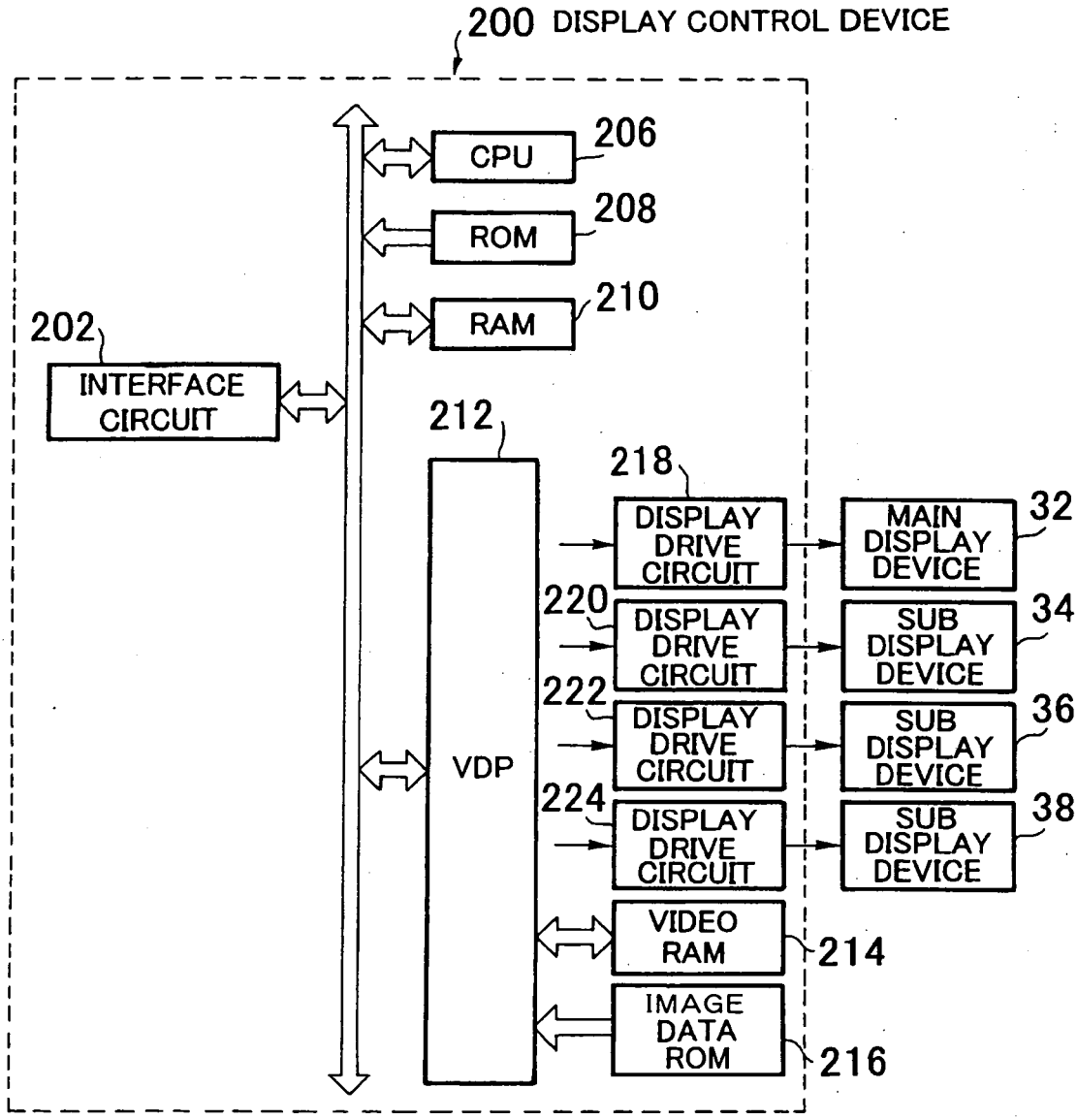
FIG. 5


200 DISPLAY CONTROL DEVICE
CPU
206
ROM
208
RAM
210
202
INTERFACE CIRCUIT
212
VDP
218
DISPLAY DRIVE CIRCUIT
220
DISPLAY DRIVE CIRCUIT
222
DISPLAY DRIVE CIRCUIT
224
DISPLAY DRIVE CIRCUIT
MAIN DISPLAY DEVICE
32
SUB DISPLAY DEVICE
34
SUB DISPLAY DEVICE
36
SUB DISPLAY DEVICE
38
VIDEO RAM
214
IMAGE DATA ROM
216