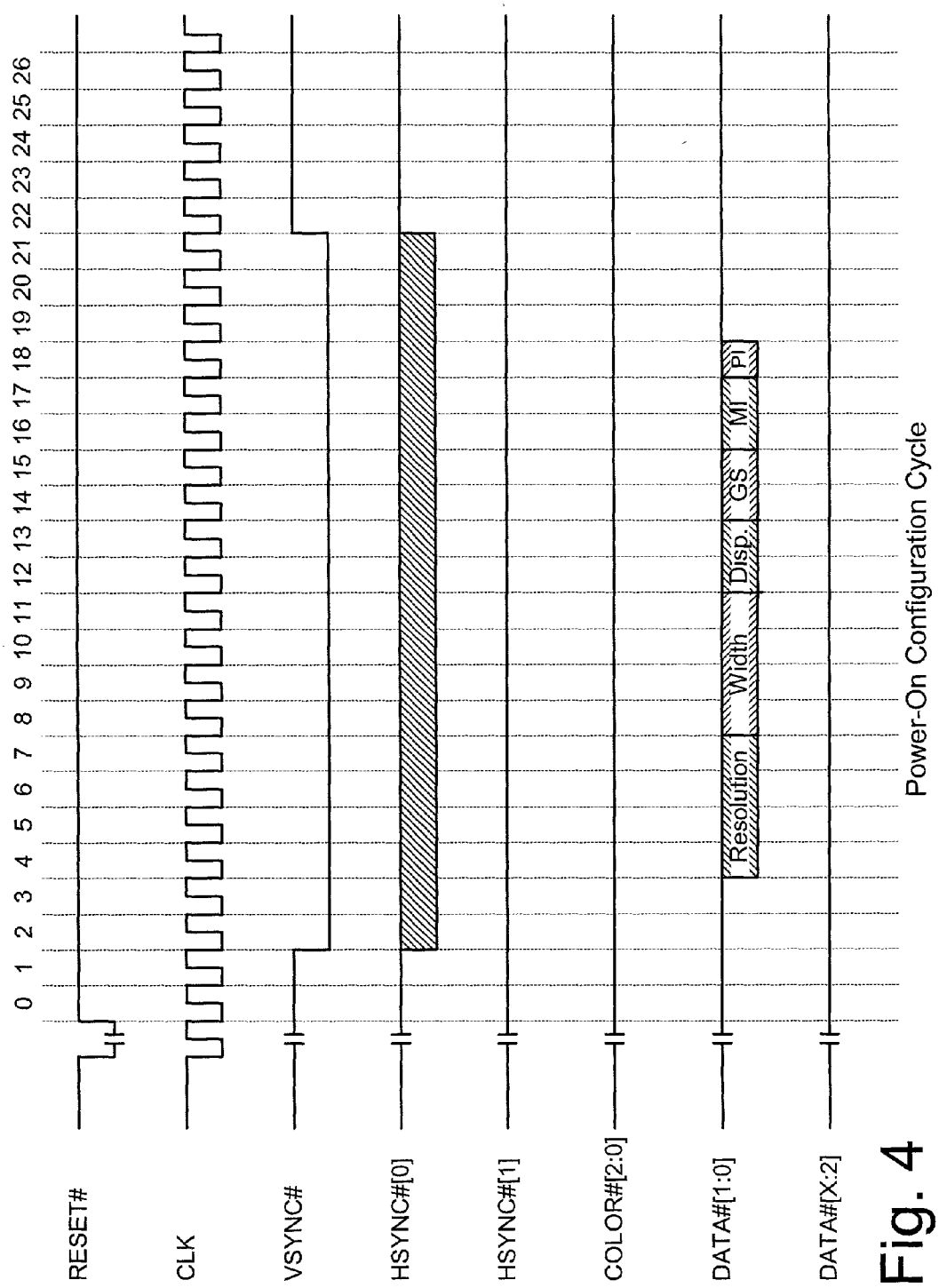
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26
RESET#
CLK
VSYNC#
HSYNC#[0]
HSYNC#[1]
COLOR#[2:0]
DATA#[1:0]
DATA#[X:2]
Resolution
Width
Disp.
GS
MI
PI
Power-On Configuration Cycle

Fig. 4