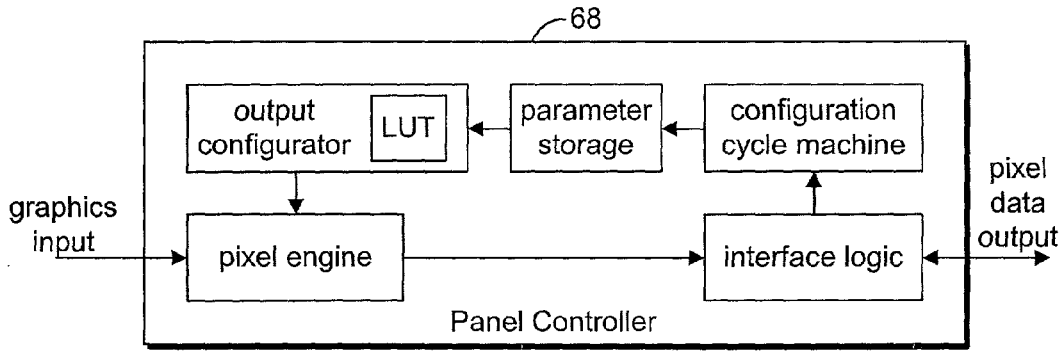
68
output configurator
LUT
parameter storage
configuration cycle machine
graphics input
pixel engine
interface logic
pixel data output
Panel Controller

Fig. 7

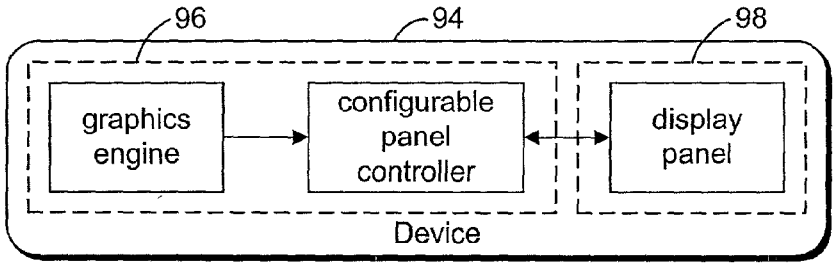
96
94
98
graphics engine
configurable panel controller
display panel
Device

Fig. 8