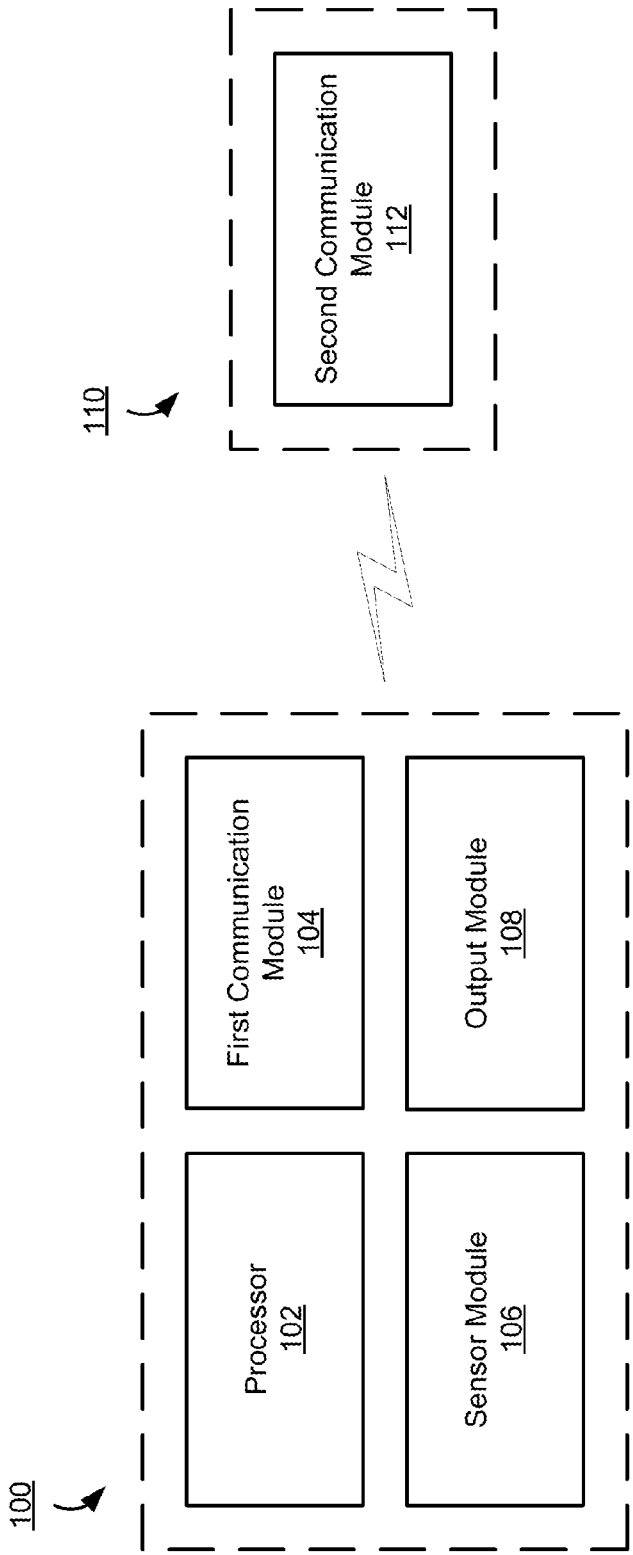
100
Processor
102
First Communication
Module
104
Sensor Module
106
Output Module
108
110
Second Communication
Module
112

FIG. 1