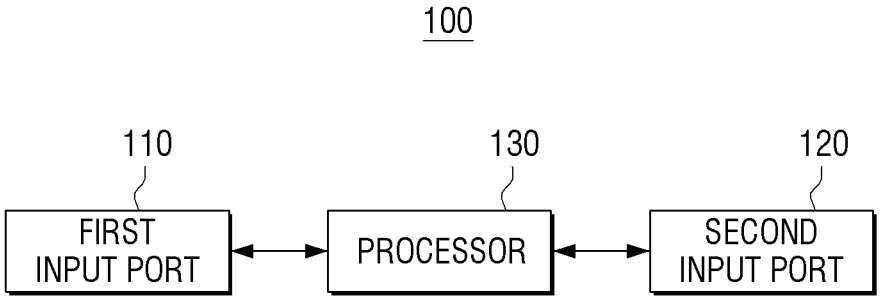
FIG. 1


100
110
130
120
FIRST INPUT PORT
PROCESSOR
SECOND INPUT PORT