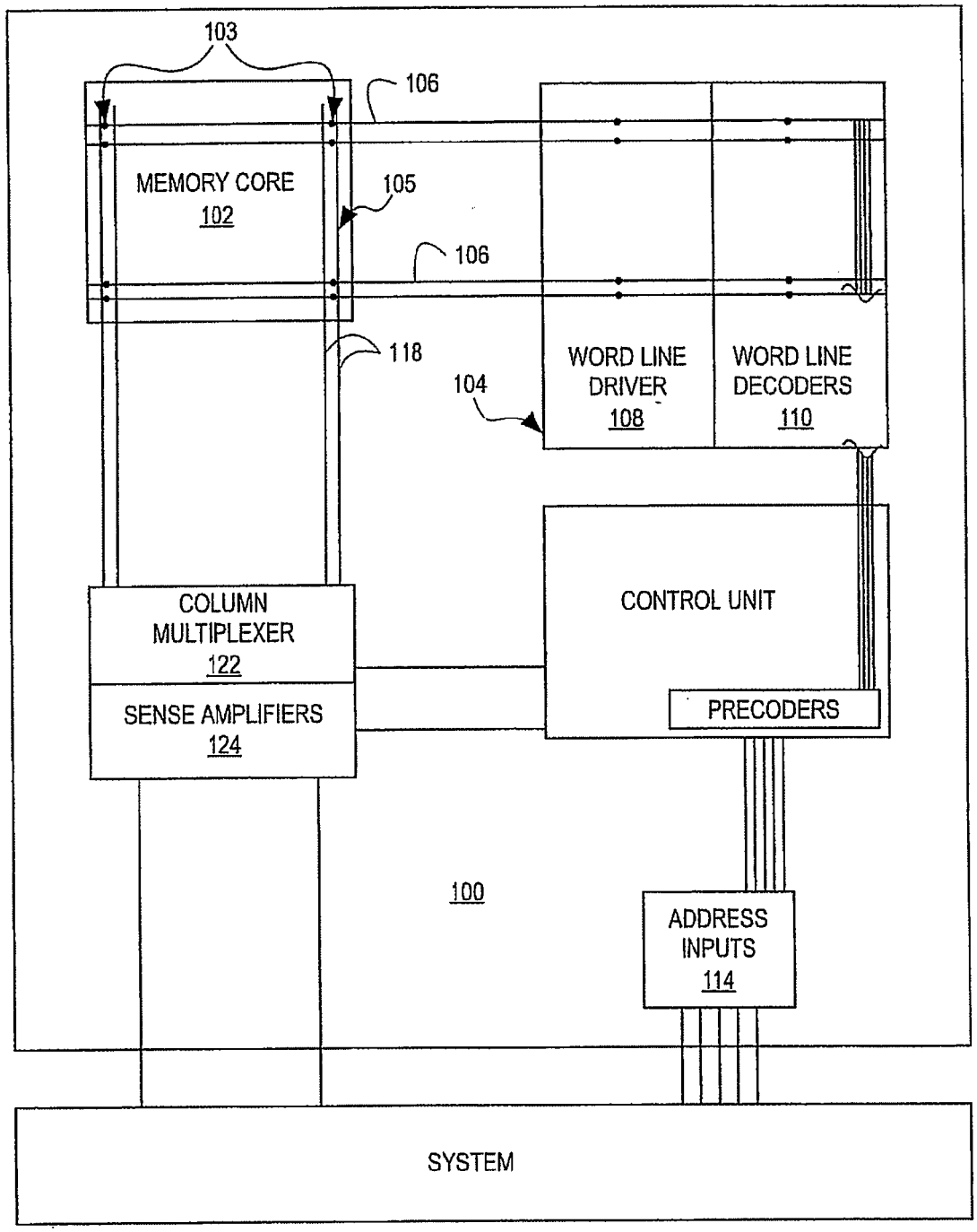
FIG. 1


103
106
MEMORY CORE
102
105
106
118
104
WORD LINE
DRIVER
108
WORD LINE
DECODERS
110
CONTROL UNIT
COLUMN
MULTIPLEXER
122
SENSE AMPLIFIERS
124
PRECODERS
100
ADDRESS
INPUTS
114
SYSTEM