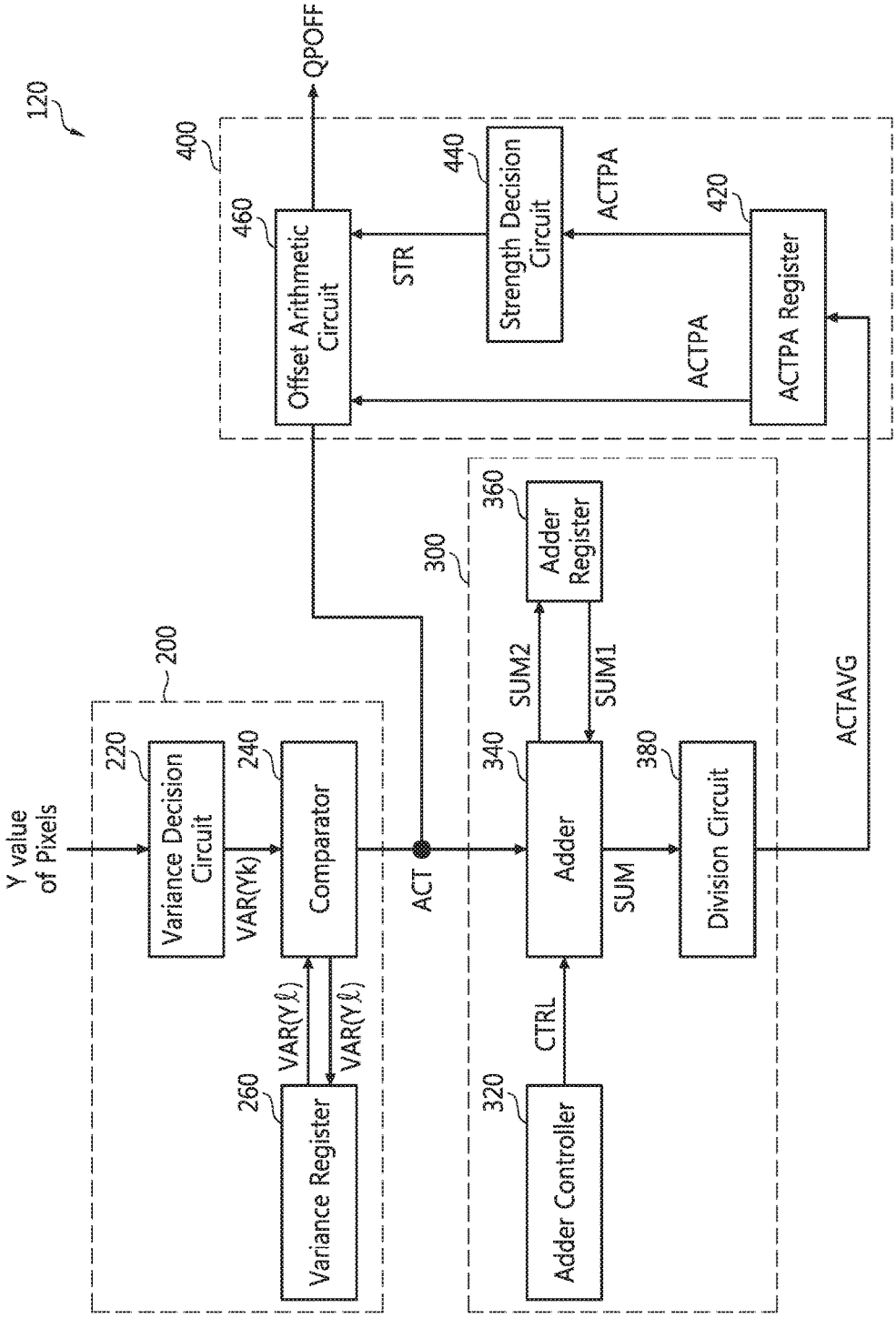
FIG. 2


120
400
QPOFF
460
Offset Arithmetic Circuit
STR
440
Strength Decision Circuit
ACTPA
420
ACTPA Register
ACTPA
300
360
Adder Register
SUM2
SUM1
200
220
Variance Decision Circuit
240
Comparator
Y value of Pixels
VAR(Yk)
ACT
340
Adder
SUM
380
Division Circuit
ACTAVG
VAR(Yℓ)
VAR(Yℓ)
CTRL
260
Variance Register
320
Adder Controller