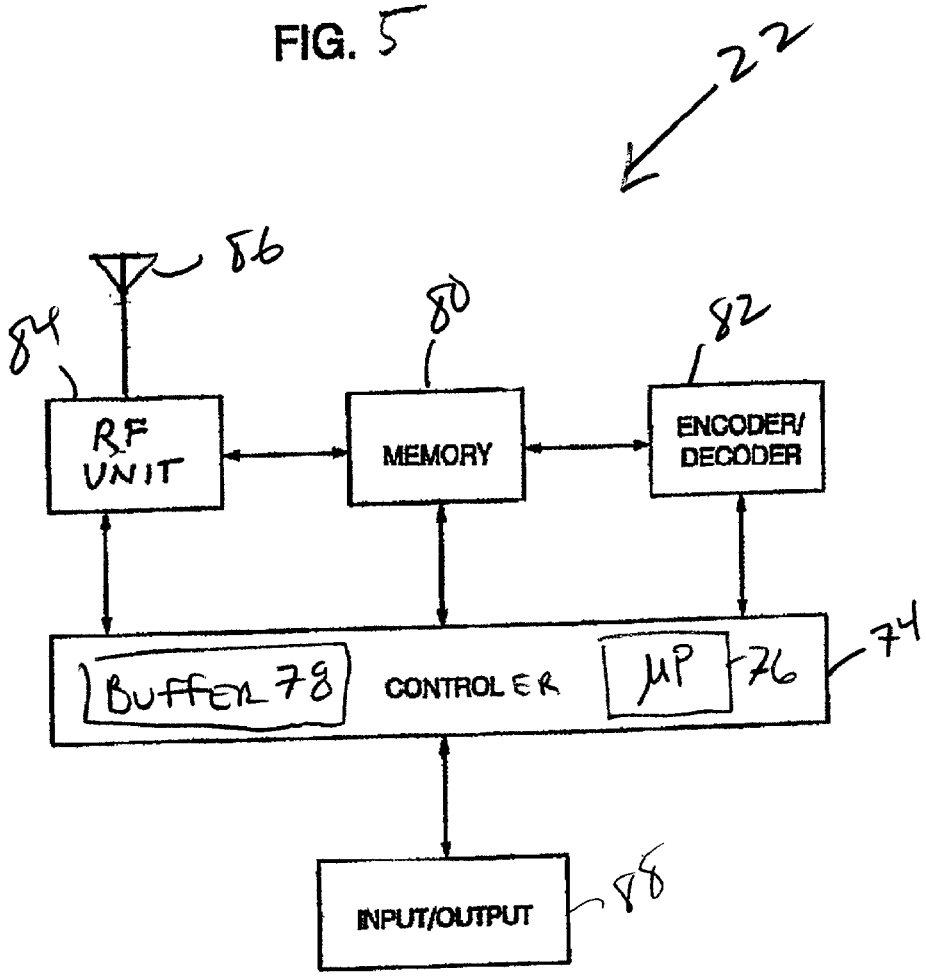
FIG. 5


22
86
84
80
82
RF UNIT
MEMORY
ENCODER/ DECODER
BUFFER 78
CONTROLER
µP
76
74
INPUT/OUTPUT
88