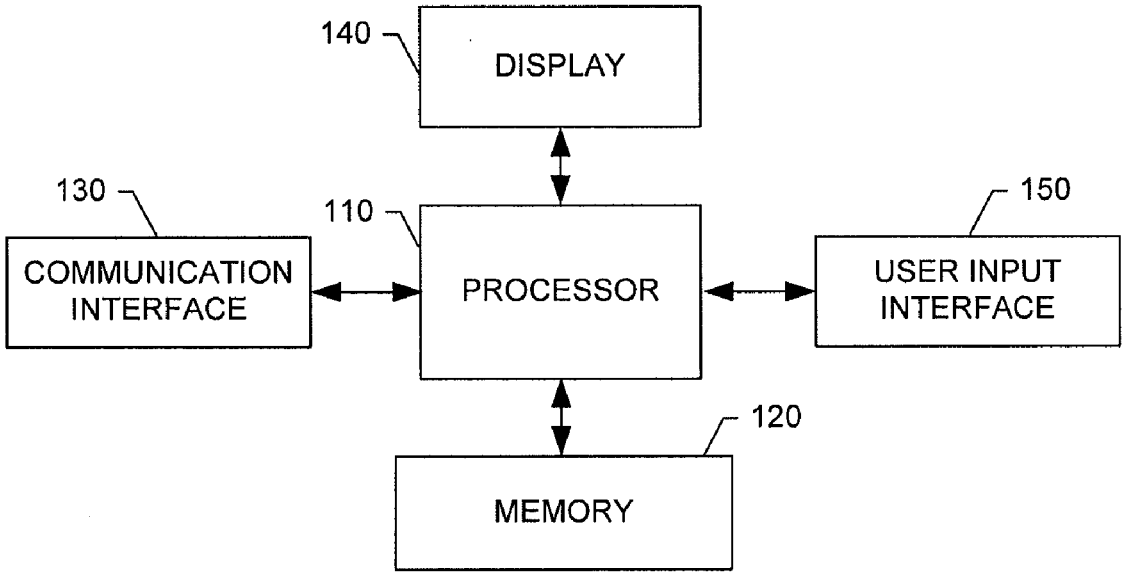
140
DISPLAY
130
110
150
COMMUNICATION INTERFACE
PROCESSOR
USER INPUT INTERFACE
120
MEMORY

FIG. 1