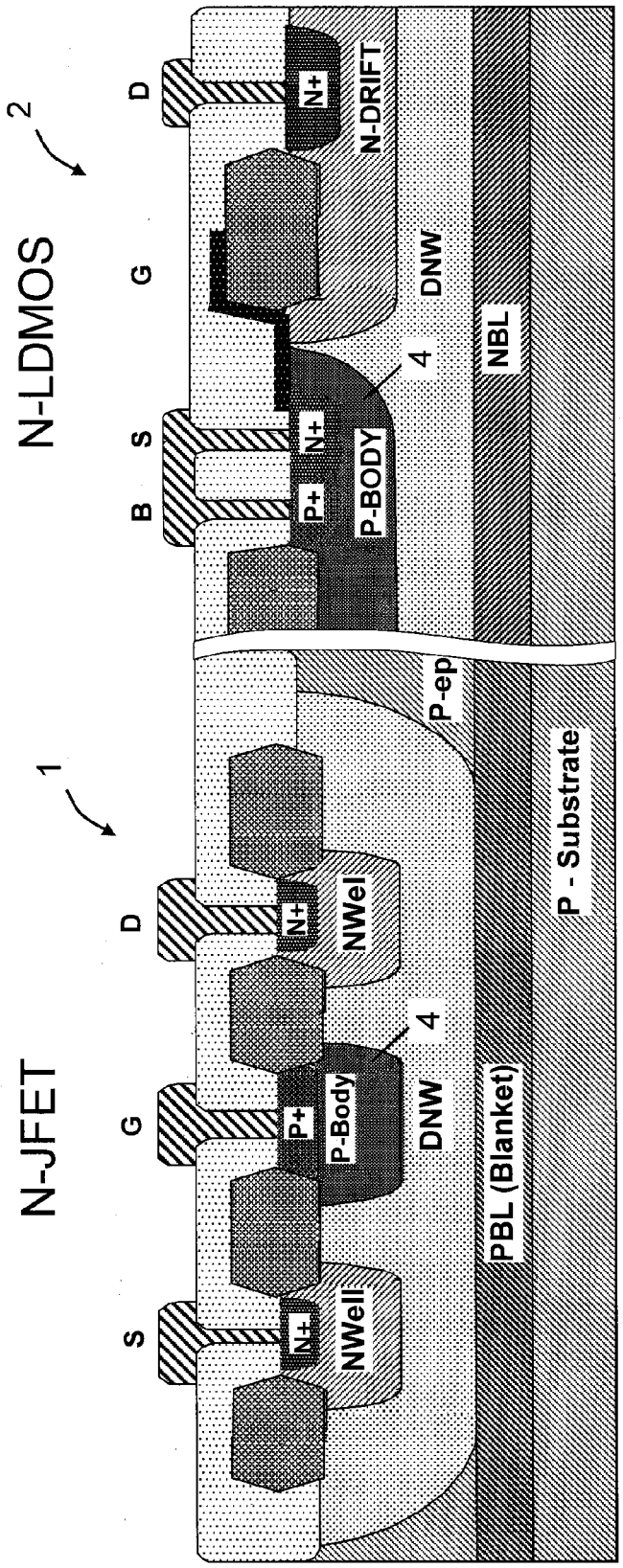
N-LDMOS
2
D
G
S
B
N+
N-DRIFT
DNW
4
NBL
P+
N+
P-BODY
P-ep
P - Substrate
N-JFET
1
D
G
S
N+
NWel
4
P+
P-Body
DNW
PBL (Blanket)
N+
NWell

Fig. 1