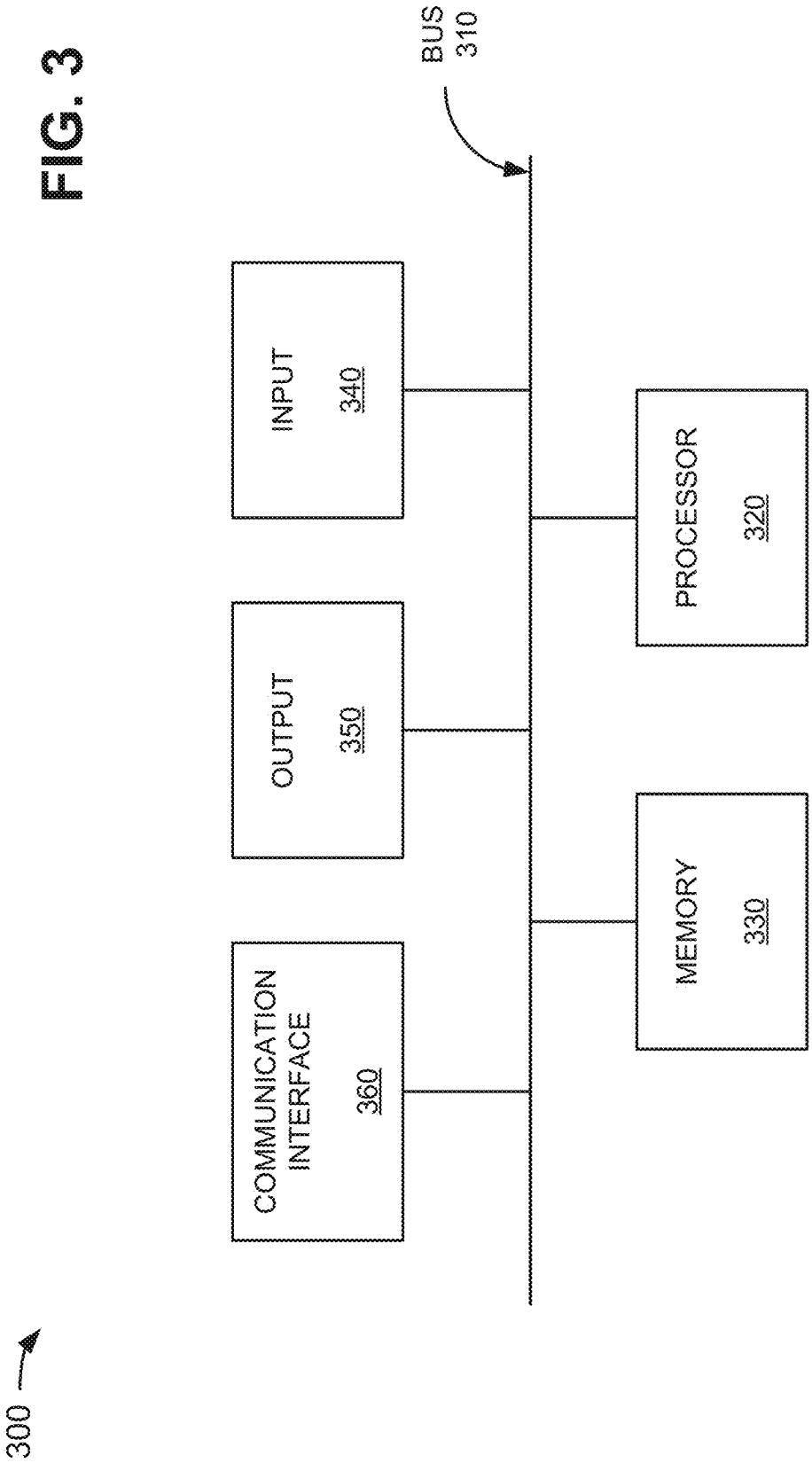
FIG. 3


300
COMMUNICATION INTERFACE
360
OUTPUT
350
INPUT
340
BUS
310
MEMORY
330
PROCESSOR
320