The server may perform requested services and provide data to the client computer(s). The server may also transmit data adapted to cause a client computer to perform a specified function, e.g., to perform a calculation, to display specified data on a screen, etc. For example, the server may transmit a request adapted to cause a client computer to perform one or more of the steps or functions of the methods and workflows described herein, including one or more of the steps or functions of FIGS. **1-2**. Certain steps or functions of the methods and workflows described herein, including one or more of the steps or functions of FIGS. **1-2**, may be performed by a server or by another processor in a network-based cloud-computing system. Certain steps or functions of the methods and workflows described herein, including one or more of the steps of FIGS. **1-2**, may be performed by a client computer in a network-based cloud computing system. The steps or functions of the methods and workflows described herein, including one or more of the steps of FIGS. **1-2**, may be performed by a server and/or by a client computer in a network-based cloud computing system, in any combination.

[0046] Systems, apparatus, and methods described herein may be implemented using a computer program product tangibly embodied in an information carrier, e.g., in a non-transitory machine-readable storage device, for execution by a programmable processor; and the method and workflow steps described herein, including one or more of the steps or functions of FIGS. **1-2**, may be implemented using one or more computer programs that are executable by such a processor. A computer program is a set of computer program instructions that can be used, directly or indirectly, in a computer to perform a certain activity or bring about a certain result. A computer program can be written in any form of programming language, including compiled or interpreted languages, and it can be deployed in any form, including as a stand-alone program or as a module, component, subroutine, or other unit suitable for use in a computing environment.

[0047] A high-level block diagram of an example computer **602** that may be used to implement systems, apparatus, and methods described herein is depicted in FIG. **6**. Computer **602** includes a processor **604** operatively coupled to a data storage device **612** and a memory **610**. Processor **604** controls the overall operation of computer **602** by executing computer program instructions that define such operations. The computer program instructions may be stored in data storage device **612**, or other computer readable medium, and loaded into memory **610** when execution of the computer program instructions is desired. Thus, the method and workflow steps or functions of FIGS. **1-2** can be defined by the computer program instructions stored in memory **610** and/or data storage device **612** and controlled by processor **604** executing the computer program instructions. For example, the computer program instructions can be implemented as computer executable code programmed by one skilled in the art to perform the method and workflow steps or functions of FIGS. **1-2**. Accordingly, by executing the computer program instructions, the processor **604** executes the method and workflow steps or functions of FIGS. **1-2**. Computer **602** may also include one or more network interfaces **606** for communicating with other devices via a network. Computer **602** may also include one or more input/output devices **608** that enable user interaction with computer **602** (e.g., display, keyboard, mouse, speakers, buttons, etc.).

[0048] Processor **604** may include both general and special purpose microprocessors, and may be the sole processor or one of multiple processors of computer **602**. Processor **604** may include one or more central processing units (CPUs), for example. Processor **604**, data storage device **612**, and/or memory **610** may include, be supplemented by, or incorporated in, one or more application-specific integrated circuits (ASICs) and/or one or more field programmable gate arrays (FPGAs).

[0049] Data storage device **612** and memory **610** each include a tangible non-transitory computer readable storage medium. Data storage device **612**, and memory **610**, may each include high-speed random access memory, such as dynamic random access memory (DRAM), static random access memory (SRAM), double data rate synchronous dynamic random access memory (DDR RAM), or other random access solid state memory devices, and may include non-volatile memory, such as one or more magnetic disk storage devices such as internal hard disks and removable disks, magneto-optical disk storage devices, optical disk storage devices, flash memory devices, semiconductor memory devices, such as erasable programmable read-only memory (EPROM), electrically erasable programmable read-only memory (EEPROM), compact disc read-only memory (CD-ROM), digital versatile disc read-only memory (DVD-ROM) disks, or other non-volatile solid state storage devices.

[0050] Input/output devices **608** may include peripherals, such as a printer, scanner, display screen, etc. For example, input/output devices **608** may include a display device such as a cathode ray tube (CRT) or liquid crystal display (LCD) monitor for displaying information to the user, a keyboard, and a pointing device such as a mouse or a trackball by which the user can provide input to computer **602**.

[0051] An image acquisition device **614** can be connected to the computer **602** to input image data (e.g., medical images) to the computer **602**. It is possible to implement the image acquisition device **614** and the computer **602** as one device. It is also possible that the image acquisition device **614** and the computer **602** communicate wirelessly through a network. In a possible embodiment, the computer **602** can be located remotely with respect to the image acquisition device **614**.

[0052] Any or all of the systems and apparatus discussed herein, as well as the machine learning based networks described herein, such as, e.g., detection model **104** and false positive reduction model **112** of FIG. **1**, network architecture **300** of FIG. **3**, and framework **400** of FIG. **4**, may be implemented using one or more computers such as computer **602**.

[0053] One skilled in the art will recognize that an implementation of an actual computer or computer system may have other structures and may contain other components as well, and that FIG. **6** is a high level representation of some of the components of such a computer for illustrative purposes.

[0054] The foregoing Detailed Description is to be understood as being in every respect illustrative and exemplary, but not restrictive, and the scope of the invention disclosed herein is not to be determined from the Detailed Description, but rather from the claims as interpreted according to the full breadth permitted by the patent laws. It is to be understood that the embodiments shown and described herein are only illustrative of the principles of the present invention and that