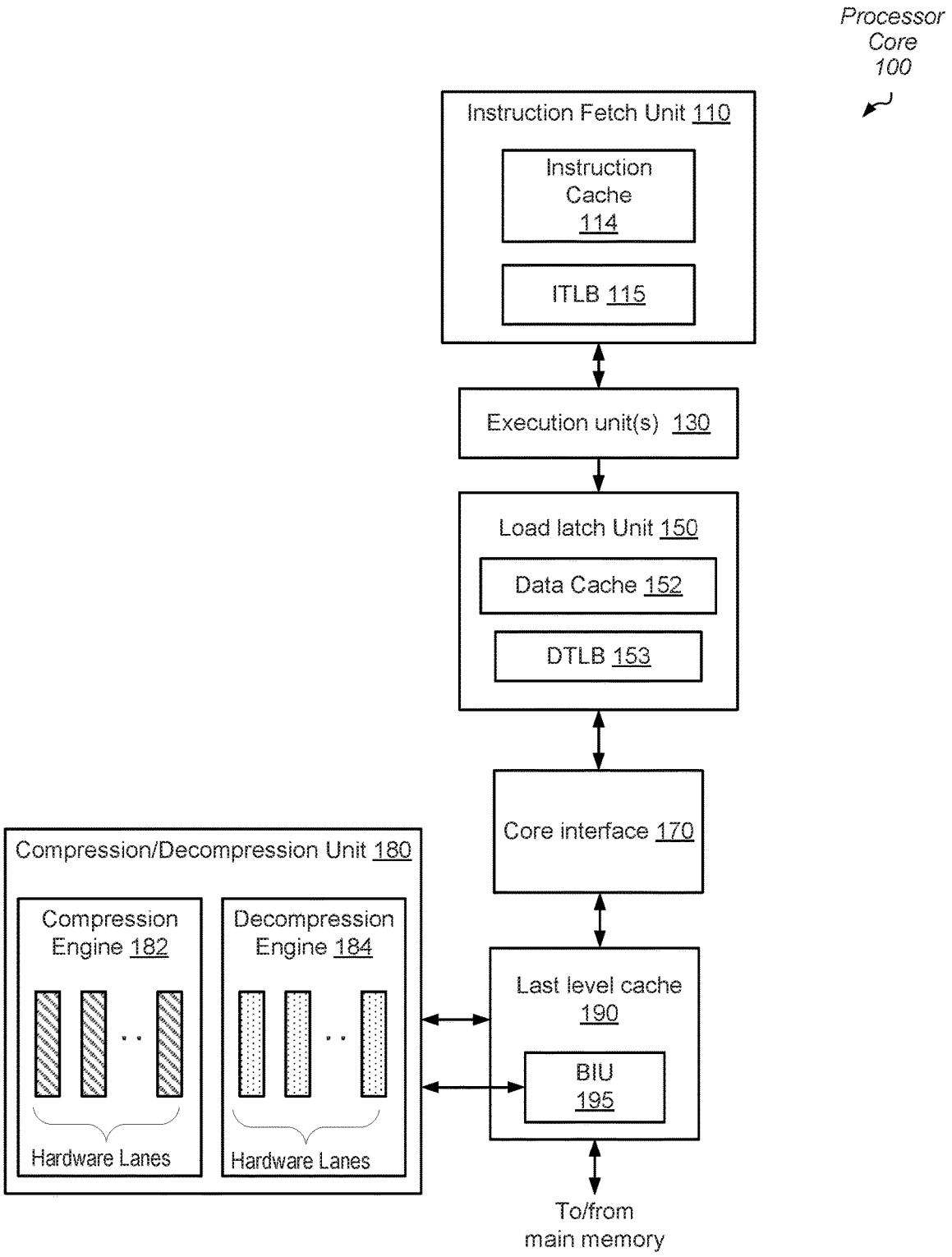
Processor Core 100
Instruction Fetch Unit 110
Instruction Cache 114
ITLB 115
Execution unit(s) 130
Load latch Unit 150
Data Cache 152
DTLB 153
Core interface 170
Compression/Decompression Unit 180
Compression Engine 182
Hardware Lanes
Decompression Engine 184
Hardware Lanes
Last level cache 190
BIU 195
To/from main memory

FIG. 1